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Ray T. Chen, Peter S. Guilfoyle

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Proceedings, 1996 International Conference on Multichip Modules ,1996 1996 IEEE Multi-Chip Module Conference ,1996 Analog Circuit Design Herman Casier, Michiel Steyaert, Arthur H.M. van Roermund, 2008-03-19 Analog Circuit Design is based on the yearly Advances in Analog Circuit Design workshop The aim of the workshop is to bring together designers of advanced analogue and RF circuits for the purpose of studying and discussing new possibilities and future developments in this field Selected topics for AACD 2007 are 1 Sensors Actuators and Power Drivers for the Automotive and Industrial Environment Tue 27 March Chaired by Herman Casier AMI Semiconductor Fellow Belgium 2 Integrated PA s from Wireline to RF Wed 28 March Chaired by Prof Michiel Steyaert Catholic University Leuven 3 Very High Frequency Front Ends Thu 29 March Chaired by Prof Arthur van Roermund Eindhoven University of Technology

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Advanced Electronic Packaging: Volume 968 Vasudeva P. Atluri,2007-04-09 Microelectronic packaging architecture evolutions are being driven by silicon technology advancements and new form factors used models and emerging technologies High performance mobile computer and communication systems will require higher I O counts greater density lower cost lighter weight and improved performance in the electronic package The book focuses on silicon technology dimension scaling and performance improvement Pb free or green assembly and system in package SIP technologies It explores the key thermomechanical failure modes and mitigating solutions associated with integration of silicon with weak interlayer dielectrics during the assembly process under bump metallurgy integrity with lead free assembly and the impact of

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