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# 13th International Conference On Parallel Architectures And Compilation Techniques

**David Kaeli, Pen-Chung Yew**



### **13th International Conference On Parallel Architectures And Compilation Techniques:**

*13th International Conference on Parallel Architectures and Compilation Techniques* International Conference on Parallel Architectures and Compilation Techniques, 2004     **PACT 2004**, 2004-01-01 PACT is a multi disciplinary conference that presents ground breaking research related to parallel systems ranging across instruction level parallelism thread level parallelism multiprocessor parallelism and distributed systems     13th International Conference on Parallel Architecture and Compilation Techniques, 2004     *Euro-Par 2014: Parallel Processing* Fernando Silva, Inês Dutra, Vitor Santos Costa, 2014-08-11 This book constitutes the refereed proceedings of the 20th International Conference on Parallel and Distributed Computing Euro Par 2014 held in Porto Portugal in August 2014 The 68 revised full papers presented were carefully reviewed and selected from 267 submissions The papers are organized in 15 topical sections support tools environments performance prediction and evaluation scheduling and load balancing high performance architectures and compilers parallel and distributed data management grid cluster and cloud computing green high performance computing distributed systems and algorithms parallel and distributed programming parallel numerical algorithms multicore and manycore programming theory and algorithms for parallel computation high performance networks and communication high performance and scientific applications and GPU and accelerator computing     *High Performance Embedded Architectures and Compilers* Yale N. Patt, Pierfrancesco Foglia, Evelyn Duesterwald, Paolo Faraboschi, Xavier Martorell, 2010-01-21 This book constitutes the refereed proceedings of the 5th International Conference on High Performance Embedded Architectures and Compilers HiPEAC 2010 held in Pisa Italy in January 2010 The 23 revised full papers presented together with the abstracts of 2 invited keynote addresses were carefully reviewed and selected from 94 submissions The papers are organized in topical sections on architectural support for concurrency compilation and runtime systems reconfigurable and customized architectures multicore efficiency reliability and power memory organization and optimization and programming and analysis of accelerators     **Algorithms and Architectures for Parallel Processing** Xiang-he Sun, Wenyu Qu, Ivan Stojmenovic, Wanlei Zhou, Zhiyang Li, Hua Guo, Geyong Min, Tingting Yang, Yulei Wu, Lei Liu, 2014-08-12 This two volume set LNCS 8630 and 8631 constitutes the proceedings of the 14th International Conference on Algorithms and Architectures for Parallel Processing ICA3PP 2014 held in Dalian China in August 2014 The 70 revised papers presented in the two volumes were selected from 285 submissions The first volume comprises selected papers of the main conference and papers of the 1st International Workshop on Emerging Topics in Wireless and Mobile Computing ETWMC 2014 the 5th International Workshop on Intelligent Communication Networks IntelNet 2014 and the 5th International Workshop on Wireless Networks and Multimedia WNM 2014 The second volume comprises selected papers of the main conference and papers of the Workshop on Computing Communication and Control Technologies in Intelligent Transportation System 3C in ITS 2014 and the Workshop on Security and Privacy in Computer and Network Systems SPCNS 2014     **Algorithms and Architectures**

**for Parallel Processing** Meikang Qiu, 2020-09-30 This three volume set LNCS 12452 12453 and 12454 constitutes the proceedings of the 20th International Conference on Algorithms and Architectures for Parallel Processing ICA3PP 2020 in New York City NY USA in October 2020 The total of 142 full papers and 5 short papers included in this proceedings volumes was carefully reviewed and selected from 495 submissions ICA3PP is covering the many dimensions of parallel algorithms and architectures encompassing fundamental theoretical approaches practical experimental projects and commercial components and systems As applications of computing systems have permeated in every aspects of daily life the power of computing system has become increasingly critical This conference provides a forum for academics and practitioners from countries around the world to exchange ideas for improving the efficiency performance reliability security and interoperability of computing systems and applications ICA3PP 2020 focus on two broad areas of parallel and distributed computing i e architectures algorithms and networks and systems and applications The Computer Engineering Handbook Vojin G. Oklobdzija, 2001-12-26 There is arguably no field in greater need of a comprehensive handbook than computer engineering The unparalleled rate of technological advancement the explosion of computer applications and the now in progress migration to a wireless world have made it difficult for engineers to keep up with all the developments in specialties outside their own References published only a few years ago are now sorely out of date The Computer Engineering Handbook changes all of that Under the leadership of Vojin Oklobdzija and a stellar editorial board some of the industry s foremost experts have joined forces to create what promises to be the definitive resource for computer design and engineering Instead of focusing on basic introductory material it forms a comprehensive state of the art review of the field s most recent achievements outstanding issues and future directions The world of computer engineering is vast and evolving so rapidly that what is cutting edge today may be obsolete in a few months While exploring the new developments trends and future directions of the field The Computer Engineering Handbook captures what is fundamental and of lasting value **The Compiler Design Handbook** Y.N. Srikant, Priti Shankar, 2002-09-25 The widespread use of object oriented languages and Internet security concerns are just the beginning Add embedded systems multiple memory banks highly pipelined units operating in parallel and a host of other advances and it becomes clear that current and future computer architectures pose immense challenges to compiler designers challenges th *Programming Languages and Systems* Zhenjiang Hu, 2009-11-24 This book constitutes the refereed proceedings of the 7th Asian Symposium on Programming Languages and Systems APLAS 2009 held in Seoul Korea in December 2009 The 21 papers presented in this volume together with 3 invited talks were carefully reviewed and selected from 56 submissions The papers are divided into topical sections on program analysis transformation and optimization type system separation logic logic and foundation theory software security and verification and software security and verification **FPGAs for Software Programmers** Dirk Koch, Frank Hannig, Daniel Ziener, 2016-06-17 This book makes powerful Field Programmable Gate Array FPGA and reconfigurable technology

accessible to software engineers by covering different state of the art high level synthesis approaches e g OpenCL and several C to gates compilers It introduces FPGA technology its programming model and how various applications can be implemented on FPGAs without going through low level hardware design phases Readers will get a realistic sense for problems that are suited for FPGAs and how to implement them from a software designer s point of view The authors demonstrate that FPGAs and their programming model reflect the needs of stream processing problems much better than traditional CPU or GPU architectures making them well suited for a wide variety of systems from embedded systems performing sensor processing to large setups for Big Data number crunching This book serves as an invaluable tool for software designers and FPGA design engineers who are interested in high design productivity through behavioural synthesis domain specific compilation and FPGA overlays Introduces FPGA technology to software developers by giving an overview of FPGA programming models and design tools as well as various application examples Provides a holistic analysis of the topic and enables developers to tackle the architectural needs for Big Data processing with FPGAs Explains the reasons for the energy efficiency and performance benefits of FPGA processing Provides a user oriented approach and a sense for where and how to apply FPGA technology

**Computer Algebra in Scientific Computing** François Boulrier,Matthew England,Timur M. Sadykov,Evgenii V. Vorozhtsov,2020-10-17 This book constitutes the refereed proceedings of the 22nd International Workshop on Computer Algebra in Scientific Computing CASC 2020 held in Linz Austria in September 2020 The conference was held virtually due to the COVID 19 pandemic The 34 full papers presented together with 2 invited talks were carefully reviewed and selected from 41 submissions They deal with cutting edge research in all major disciplines of computer algebra The papers cover topics such as polynomial algebra symbolic and symbolic numerical computation applications of symbolic computation for investigating and solving ordinary differential equations applications of CAS in the investigation and solution of celestial mechanics problems and in mechanics physics and robotics

Advanced Computing, Networking and Informatics- Volume 2 Malay Kumar Kundu,Durga Prasad Mohapatra,Amit Konar,Aruna Chakraborty,2014-05-26 Advanced Computing Networking and Informatics are three distinct and mutually exclusive disciplines of knowledge with no apparent sharing overlap among them However their convergence is observed in many real world applications including cyber security internet banking healthcare sensor networks cognitive radio pervasive computing amidst many others This two volume proceedings explore the combined use of Advanced Computing and Informatics in the next generation wireless networks and security signal and image processing ontology and human computer interfaces HCI The two volumes together include 148 scholarly papers which have been accepted for presentation from over 640 submissions in the second International Conference on Advanced Computing Networking and Informatics 2014 held in Kolkata India during June 24 26 2014 The first volume includes innovative computing techniques and relevant research results in informatics with selective applications in pattern recognition signal image processing and HCI The second volume on the other hand demonstrates the possible scope

of the computing techniques and informatics in wireless communications networking and security

**Algorithms and Architectures for Parallel Processing** Sang-Soo Yeo, Jong Hyuk Park, Laurence Tianruo Yang, Ching-Hsien Hsu, 2010-05-29 It is our great pleasure to present the proceedings of the symposia and workshops on parallel and distributed computing and applications associated with the ICA3PP 2010 conference. These symposia and workshops provide vibrant opportunities for researchers and industry practitioners to share their research experience, original research results and practical development experiences in the new challenging research areas of parallel and distributed computing technologies and applications. It was the first time that the ICA3PP conference series added symposia and workshops to its program in order to provide a wide range of topics that extend beyond the main conferences. The goal was to provide a better coverage of emerging research areas and also forums for focused and stimulating discussions. With this objective in mind, we selected three workshops to accompany the ICA3PP 2010 conference: FPDC 2010 the 2010 International Symposium on Frontiers of Parallel and Distributed Computing, HPCTA 2010 the 2010 International Workshop on High Performance Computing Technologies and Applications, M2A 2010 the 2010 International Workshop on Multicore and Multithreaded Architectures and Algorithms. Each of the symposia/workshops focused on a particular theme and complemented the spectrum of the main conference. All papers published in the workshops/proceedings were selected by the Program Committee on the basis of referee reports. Each paper was reviewed by independent referees who judged the papers for originality, quality, contribution, presentation and consistency with the theme of the workshops.

**Euro-Par 2009 - Parallel Processing** Dick Epema, 2009-08-17 This book constitutes the refereed proceedings of the 15th International Conference on Parallel Computing Euro-Par 2009 held in Delft, The Netherlands, in August 2009. The 85 revised papers presented were carefully reviewed and selected from 256 submissions. The papers are organized in topical sections on support tools and environments, performance prediction and evaluation, scheduling and load balancing, high performance architectures and compilers, parallel and distributed databases, grid, cluster and cloud computing, peer-to-peer computing, distributed systems and algorithms, parallel and distributed programming, parallel numerical algorithms, multicore and manycore programming, theory and algorithms for parallel computation, high performance networks and mobile and ubiquitous computing.

**Advanced Parallel Processing Technologies** Yunji Chen, Paolo Ienne, Qing Ji, 2015-08-14 This book constitutes the proceedings of the 11th International Symposium on Advanced Parallel Processing Technologies APPT 2015 held in Jinan, China, in August 2015. The 8 papers presented in this volume were carefully reviewed and selected from 24 submissions. They deal with the recent advances in big data processing, parallel architectures and systems, parallel software, parallel algorithms and applications, and distributed and cloud computing.

**A Practical Programming Model for the Multi-Core Era** Barbara Chapman, Weimin Zheng, Guang R. Gao, Mitsuhiro Sato, Eduard Ayguadé, Dongsheng Wang, 2008-07-10 The Third International Workshop on OpenMP IWOMP 2007 was held at Beijing, China. This year's workshop continued its tradition of being the premier opportunity to

learn more about OpenMP to obtain practical experience and to interact with OpenMP users and developers The workshop also served as a forum for presenting insights gained by practical experience as well as research ideas and results related to OpenMP A total of 28 submissions were received in response to a call for papers Each submission was evaluated by three reviewers and additional reviews were received for some papers Based on the feedback received 22 papers were accepted for inclusion in the proceedings Of the 22 papers 14 were accepted as full papers We also accepted eight short papers for each of which there was an opportunity to give a short presentation at the workshop followed by poster demonstrations Each paper was judged according to its originality innovation readability and relevance to the expected audience Due to the limited scope and time of the workshop and the high number of submissions received only 50% of the total submissions were able to be included in the final program In addition to the contributed papers the IWOMP 2007 program featured several keynote and banquet speakers Trevor Mudge Randy Brown and Shah Sanjiv These speakers were selected due to their significant contributions and reputation in the field A tutorial session and labs were also associated with IWOMP 2007

**Encyclopedia of Business Analytics and Optimization** Wang, John, 2014-02-28 As the age of Big Data emerges it becomes necessary to take the five dimensions of Big Data volume variety velocity volatility and veracity and focus these dimensions towards one critical emphasis value The Encyclopedia of Business Analytics and Optimization confronts the challenges of information retrieval in the age of Big Data by exploring recent advances in the areas of knowledge management data visualization interdisciplinary communication and others Through its critical approach and practical application this book will be a must have reference for any professional leader analyst or manager interested in making the most of the knowledge resources at their disposal

**Speculative Execution in High Performance Computer Architectures** David Kaeli, Pen-Chung Yew, 2005-05-26 Until now there were few textbooks that focused on the dynamic subject of speculative execution a topic that is crucial to the development of high performance computer architectures Speculative Execution in High Performance Computer Architectures describes many recent advances in speculative execution techniques It covers cutting edge research

*Applications of Zero-Suppressed Decision Diagrams* Jon T. Butler, Tsutomu Sasao, 2022-05-31 A zero suppressed decision diagram ZDD is a data structure to represent objects that typically contain many zeros Applications include combinatorial problems such as graphs circuits faults and data mining This book consists of four chapters on the applications of ZDDs The first chapter by Alan Mishchenko introduces the ZDD It compares ZDDs to BDDs showing why a more compact representation is usually achieved in a ZDD The focus is on sets of subsets and on sum of products SOP expressions Methods to generate all the prime implicants PIs and to generate irredundant SOPs are shown A list of papers on the applications of ZDDs is also presented In the appendix ZDD procedures in the CUDD package are described The second chapter by Tsutomu Sasao shows methods to generate PIs and irredundant SOPs using a divide and conquer method This chapter helps the reader to understand the methods presented in the first

chapter The third chapter by Shin Ichi Minato introduces the frontier based method that efficiently enumerates certain subsets of a graph The final chapter by Shinobu Nagayama shows a method to match strings of characters This is important in routers for example where one must match the address information of an internet packet to the proper output port It shows that ZDDs are more compact than BDDs in solving this important problem Each chapter contains exercises and the appendix contains their solutions Table of Contents Preface Acknowledgments Introduction to Zero Suppressed Decision Diagrams Efficient Generation of Prime Implicants and Irredundant Sum of Products Expressions The Power of Enumeration BDD ZDD Based Algorithms for Tackling Combinatorial Explosion Regular Expression Matching Using Zero Suppressed Decision Diagrams Authors and Editors Biographies Index



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