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I/O die

12 memory controllers

PCIe® Gen 5 controllers

Infinity Fabric™ controllers

SATA controllers

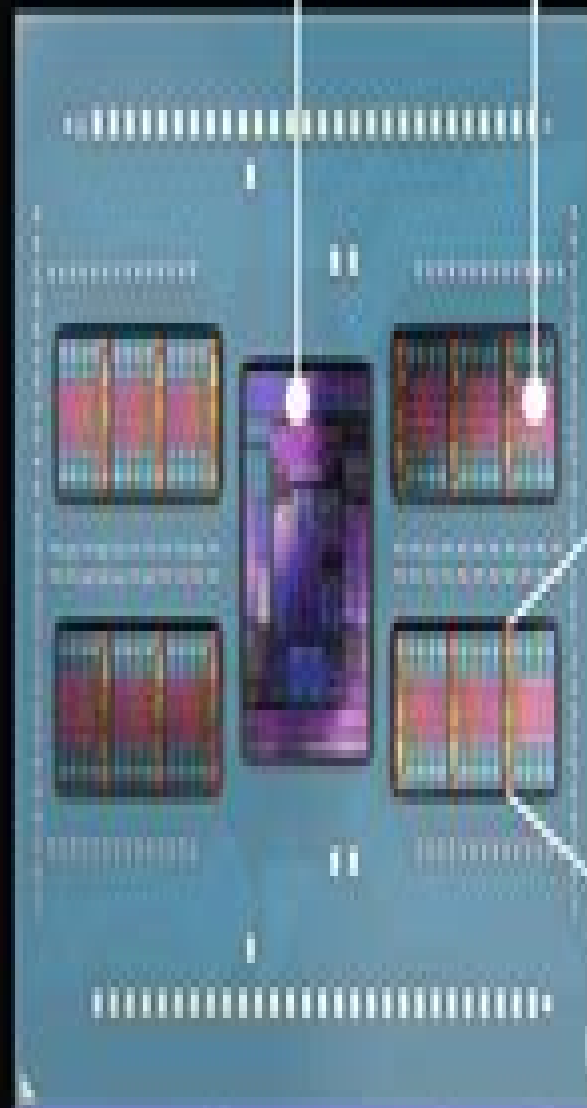
CXL™ controllers

AMD Secure Processor

CPU die

8 cores per die

Up to 12 dies per processor



CPU die detail

8 "Zen 4" cores

1 MB L2 cache per core

Shared 32 MB L3 cache



# 1632bit Embedded Processors

**Linley Gwennap, Joseph Byrne**



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