



Student Symposium on Electron Devices and Solid-State Circuit (s-EDSSC)

Venue: 9:30 am-4:10 pm, Oct 25, 2020 on Zoom

Topics include:

- A Flipped-Voltage-Follower-Based Low-Dropout Regulator with Signal- and Transient-Current Boosting
- Optimization of SiC Floating Island and Thick Bottom Oxide Trench MOSFET (FITMOS) via TCAD Simulation
- Compact Modeling of Current-Assisted Photonic Demodulator (CAPD) for Time-of-Flight CMOS Image Sensor
- Survey of temperature dependence of the damping parameter in the ferrimagnet $Gd_5Fe_5O_{12}$
- Room-temperature valley transistor with Weyl semiconductor
- Facile Fabrication of Cu-Decorated Laser-induced Graphene for Wearable Glucose Monitoring
- Designing Generic Cryogenic CMOS Models for Quantum Computing Applications
- Simulation on Efficient High Frequency Spin Wave Excitation with Undulating Ferromagnetic Film
- Highly Secured Graphene Physical Unclonable Function Tags
- Complementary Logic Inverter based on MoS₂ FET Technology
- High -performance tellurium FET with high-work-function metal contacts
- Ultralow-k interconnect by CNT-patterned porous dielectric and intra-layer h-BN-capped air-gaps
- A Wearable Respiratory Monitor System

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Organized by:

IEEE Hong Kong Joint Chapter of Electron Devices and Solid-State Circuits

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Proceedings ,1998 **IEEE Hong Kong Electron Devices Meeting** ,2001 Proceedings 1998 IEEE Hong Kong Electron Devices Meeting ,1998 *Proceedings 1998 IEEE Hong Kong Electron Devices Meeting* ,1998 **Proceedings IEEE Hong Kong Electron Devices Meeting** ,2001 *Cumulated Index to the Books* ,1999 Silicide Technology for Integrated Circuits Institution of Electrical Engineers,2004-12-21 This is the first book to provide guidance on the development and application of metal silicide technology as it emerges from the scientific to the prototype and manufacturing stages Other key topics covered are fundamentals present and future silicide technology for Si based devices and characterisation methods Suitable for engineers and students in microelectronics *Sensor Systems Simulations* Willem Dirk van Driel,Oliver Pyper,Cornelia Schumann,2019-06-18 This book describes for readers various technical outcomes from the EU project IoSense The authors discuss sensor integration including LEDs dust sensors LIDAR for automotive driving and 8 more demonstrating their use in simulations for the design and fabrication of sensor systems Readers will benefit from the coverage of topics such as sensor technologies for both discrete and integrated innovative sensor devices suitable for high volume production electrical mechanical security and software resources for integration of sensor system components into IoT systems and IoT enabling systems and IoT sensor system reliability Describes from component to system level simulation how to use the available simulation techniques for reaching a proper design with good performance Explains how to use simulation techniques such as Finite Elements Multi body Dynamic stochastics and many more in the virtual design of sensor systems Demonstrates the integration of several sensor solutions thermal dust occupancy distance awareness and more into large scale system solutions in several industrial domains Lighting automotive transport and more Includes state of the art simulation techniques both multi scale and multi physics for use in the electronic industry

Fundamentals of Electromigration-Aware Integrated Circuit Design Jens Lienig,Susann Rothe,Matthias Thiele,2025-02-25 The book provides a comprehensive overview of electromigration and its effects on the reliability of electronic circuits This second edition has been updated to introduce recent advancements in the understanding of the physical process of electromigration which gives the reader the knowledge for adopting appropriate counter measures A comprehensive set of options is presented for modifying the present IC design methodology to prevent electromigration Finally the authors show how specific effects can be exploited in present and future technologies to reduce electromigration s negative impact on circuit reliability Thermal and Power Management of Integrated Circuits Arman Vassighi,Manoj Sachdev,2006-06-01 In Thermal and Power Management of Integrated Circuits power and thermal management issues in integrated circuits during normal operating conditions and stress operating conditions are addressed Thermal management in VLSI circuits is becoming an integral part of the design test and manufacturing Proper thermal management is the key to achieve high performance quality and reliability Performance and reliability of integrated circuits are strong functions of the

junction temperature A small increase in junction temperature may result in significant reduction in the device lifetime This book reviews the significance of the junction temperature as a reliability measure under nominal and burn in conditions The latest research in the area of electro thermal modeling of integrated circuits will also be presented Recent models and associated CAD tools are covered and various techniques at the circuit and system levels are reviewed Subsequently the authors provide an insight into the concept of thermal runaway and how it may best be avoided A section on low temperature operation of integrated circuits concludes the book

MOS Devices for Low-Voltage and Low-Energy Applications Yasuhisa Omura, Abhijit Mallik, Naoto Matsuo, 2017-02-28 Helps readers understand the physics behind MOS devices for low voltage and low energy applications Based on timely published and unpublished work written by expert authors Discusses various promising MOS devices applicable to low energy environmental and biomedical uses Describes the physical effects quantum tunneling of MOS devices Demonstrates the performance of devices helping readers to choose right devices applicable to an industrial or consumer environment Addresses some Ge based devices and other compound material based devices for high frequency applications and future development of high performance devices Seemingly innocuous everyday devices such as smartphones tablets and services such as on line gaming or internet keyword searches consume vast amounts of energy Even when in standby mode all these devices consume energy The upcoming Internet of Things IoT is expected to deploy 60 billion electronic devices spread out in our homes cars and cities Britain is already consuming up to 16 per cent of all its power through internet use and this rate is doubling every four years According to The UK's Daily Mail May 2015 if usage rates continue all of Britain's power supply could be consumed by internet use in just 20 years In 2013 U S data centers consumed an estimated 91 billion kilowatt hours of electricity corresponding to the power generated by seventeen 1000 megawatt nuclear power plants Data center electricity consumption is projected to increase to roughly 140 billion kilowatt hours annually by 2020 the equivalent annual output of 50 nuclear power plants Natural Resources Defense Council USA Feb 2015 All these examples stress the urgent need for developing electronic devices that consume as little energy as possible The book *MOS Devices for Low Voltage and Low Energy Applications* explores the different transistor options that can be utilized to achieve that goal It describes in detail the physics and performance of transistors that can be operated at low voltage and consume little power such as subthreshold operation in bulk transistors fully depleted SOI devices tunnel FETs multigate and gate all around MOSFETs Examples of low energy circuits making use of these devices are given as well The book *MOS Devices for Low Voltage and Low Energy Applications* is a good reference for graduate students researchers semiconductor and electrical engineers who will design the electronic systems of tomorrow Dr Jean Pierre Colinge Taiwan Semiconductor Manufacturing Company TSMC The authors present a creative way to show how different MOS devices can be used for low voltage and low power applications They start with Bulk MOSFET following with SOI MOSFET FinFET gate all around MOSFET Tunnel FET and others It is presented the physics behind the devices models simulations experimental results and

applications This book is interesting for researchers graduate and undergraduate students The low energy field is an important topic for integrated circuits in the future and none can stay out of this Prof Joao A Martino University of Sao Paulo Brazil

Physical Model and Applications of High-Efficiency Electro-Optical Conversion Devices - Volume II Feng Chi,Qiang Xu,Dan Luo,2023-03-31 [Index of Conference Proceedings](#) British Library. Document Supply Centre,2003

[Proceedings](#) ,2001 **Power Electronics in Renewable Energy Systems and Smart Grid** Bimal K. Bose,2019-08-06

The comprehensive and authoritative guide to power electronics in renewable energy systems Power electronics plays a significant role in modern industrial automation and high efficiency energy systems With contributions from an international group of noted experts Power Electronics in Renewable Energy Systems and Smart Grid Technology and Applications offers a comprehensive review of the technology and applications of power electronics in renewable energy systems and smart grids The authors cover information on a variety of energy systems including wind solar ocean and geothermal energy systems as well as fuel cell systems and bulk energy storage systems They also examine smart grid elements modeling simulation control and AI applications The book s twelve chapters offer an application oriented and tutorial viewpoint and also contain technology status review In addition the book contains illustrative examples of applications and discussions of future perspectives This important resource Includes descriptions of power semiconductor devices two level and multilevel converters HVDC systems FACTS and more Offers discussions on various energy systems such as wind solar ocean and geothermal energy systems and also fuel cell systems and bulk energy storage systems Explores smart grid elements modeling simulation control and AI applications Contains state of the art technologies and future perspectives Provides the expertise of international authorities in the field Written for graduate students professors in power electronics and industry engineers Power Electronics in Renewable Energy Systems and Smart Grid Technology and Applications offers an up to date guide to technology and applications of a wide range of power electronics in energy systems and smart grids

Handbook of Thin Film Deposition Dominic Schepis,Krishna Seshan,2024-10-08 Handbook of Thin Film Deposition Fifth Edition is a comprehensive reference focusing on thin film technologies and applications used in the semiconductor industry When pursuing patents there is a phase called reduction to practice where the idea for a technology transitions from a concept to actual use The section Thin Film Reduction to Practice includes chapters that review the most relevant methods to fabricate thin films towards practical applications Then the latest applications of thin film deposition technologies are discussed Handbook of Thin Film Deposition 5th Edition is suitable for materials scientists and engineers in academia and working in semiconductor R D Offers a practical survey of thin film technologies including design fabrication and reliability Covers core processes and applications in the semiconductor industry and discusses latest advances in new thin film development Features new chapters that review methods on front end and back end thin films

[High-Frequency GaN Electronic Devices](#) Patrick Fay,Debdeep Jena,Paul Maki,2019-08-01 This book brings together recent research by scientists and device

engineers working on both aggressively scaled conventional transistors as well as unconventional high frequency device concepts in the III N material system Device concepts for mm wave to THz operation based on deeply scaled HEMTs as well as distributed device designs based on plasma wave propagation in polarization induced 2DEG channels tunneling and hot carrier injection are discussed in detail In addition advances in the underlying materials science that enable these demonstrations and advancements in metrology that permit the accurate characterization and evaluation of these emerging device concepts are also included Targeting readers looking to push the envelope in GaN based electronics device research this book provides a current comprehensive treatment of device concepts and physical phenomenology suitable for applying GaN and related materials to emerging ultra high frequency applications Offers readers an integrated treatment of the state of the art in both conventional i e HEMT scaling as well as unconventional device architectures suitable for amplification and signal generation in the mm wave and THz regime using GaN based devices written by authors that are active and widely known experts in the field Discusses both conventional scaled HEMTs into the deep mm wave as well as unconventional approaches to address the mm wave and THz regimes Provides vertically integrated coverage including materials science that enables these recent advances as well as device physics Includes fundamental physics as well as numerical simulations and experimental realizations

Dielectric Materials for Electrical Engineering Juan Martinez-Vega, 2013-03-04 The object of this book is to provide a comprehensive reference source for the numerous scientific communities engineers researchers students etc in various disciplines which require detailed information in the field of dielectric materials Part 1 focuses on physical properties electrical ageing and modeling including topics such as the physics of charged dielectric materials conduction mechanisms dielectric relaxation space charge electric ageing and end of life EOL models and dielectric experimental characterization Part 2 examines applications of specific relevance to dielectric materials insulating oils for transformers electro rheological fluids electrolytic capacitors ionic membranes photovoltaic conversion dielectric thermal control coatings for geostationary satellites plastics recycling and piezoelectric polymers

RF and mm-Wave Power Generation in Silicon Hua Wang, Kaushik Sengupta, 2015-12-10 RF and mm Wave Power Generation in Silicon presents the challenges and solutions of designing power amplifiers at RF and mm Wave frequencies in a silicon based process technology It covers practical power amplifier design methodologies energy and spectrum efficient power amplifier design examples in the RF frequency for cellular and wireless connectivity applications and power amplifier and power generation designs for enabling new communication and sensing applications in the mm Wave and THz frequencies With this book you will learn Power amplifier design fundamentals and methodologies Latest advances in silicon based RF power amplifier architectures and designs and their integration in wireless communication systems State of the art mm Wave THz power amplifier and power generation circuits and systems in silicon Extensive coverage from fundamentals to advanced design topics focusing on various layers of abstraction from device modeling and circuit design strategy to advanced digital and mixed signal

architectures for highly efficient and linear power amplifiers New architectures for power amplifiers in the cellular and wireless connectivity covering detailed design methodologies and state of the art performances Detailed design techniques trade off analysis and design examples for efficiency enhancement at power back off and linear amplification for spectrally efficient non constant envelope modulations Extensive coverage of mm Wave power generation techniques from the early days of the 60 GHz research to current state of the art reconfigurable digital mm Wave PA architectures Detailed analysis of power generation challenges in the higher mm Wave and THz frequencies and novel technical solutions for a wide range for potential applications including ultrafast wireless communication to sensing imaging and spectroscopy Contributions from the world class experts from both academia and industry

VLSI Design and Test for Systems Dependability Shojiro Asai, 2018-07-20 This book discusses the new roles that the VLSI very large scale integration of semiconductor circuits is taking for the safe secure and dependable design and operation of electronic systems The book consists of three parts Part I as a general introduction to this vital topic describes how electronic systems are designed and tested with particular emphasis on dependability engineering where the simultaneous assessment of the detrimental outcome of failures and cost of their containment is made This section also describes the related research project Dependable VLSI Systems in which the editor and authors of the book were involved for 8 years Part II addresses various threats to the dependability of VLSIs as key systems components including time dependent degradations variations in device characteristics ionizing radiation electromagnetic interference design errors and tampering with discussion of technologies to counter those threats Part III elaborates on the design and test technologies for dependability in such applications as control of robots and vehicles data processing and storage in a cloud environment and heterogeneous wireless telecommunications This book is intended to be used as a reference for engineers who work on the design and testing of VLSI systems with particular attention to dependability It can be used as a textbook in graduate courses as well Readers interested in dependable systems from social and industrial economic perspectives will also benefit from the discussions in this book

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