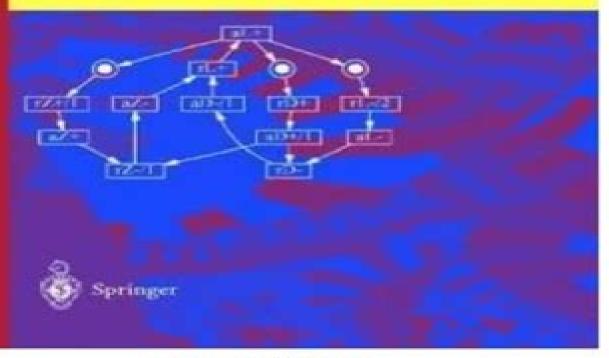Jordi Cortadella
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Grzegorz Rozenberg (Eds.)

Concurrency and Hardware Design

Advances in Petri Nets

Concurrency And Hardware Design Advances In Petri Nets

Jordi Cortadella,Alex Yakovlev,Grzegorz Rozenberg

Concurrency And Hardware Design Advances In Petri Nets:

Concurrency and Hardware Design Jordi Cortadella,Alex Yakovlev,Grzegorz Rozenberg,2003-07-01 As CMOS semiconductor technology strides towards billions of transistors on a single die new problems arise on the way They are concerned with the minishing fabrication process features which a ect for example the gate to wire delay ratio They manifest themselves in greater variations of size and operating parameters of devices which put the overall reliability of systems at risk And most of all they have tremendous impact on design productivity where the costs of utilizing the growing silicon real estate rocket to billions of dollars that have to be spent on design veri cation and testing All such problems call for new sign approaches and models for digital systems Furthermore new developments in non CMOS technologies such as single electron transistors rapid single quantum devices quantum dot cells molecular devices etc add extra demand for new research in system design methodologies What kind of models and design methodologies will be required to build systems in all these new technologies Answering this question even for each particular type of new technology generation is not easy especially because sometimes it is not even clear what kind of elementary devices are feasible there This problem is of an interdisciplinary nature It requires an bridges between di erent scienti c communities The bridges must be built very quickly and be maximally exible to accommodate changes taking place in a logarithmic timescale   *Concurrency and Hardware Design* Jordi Cortadella,Alex Yakovlev,Grzegorz Rozenberg,2002-11-26 As CMOS semiconductor technology strides towards billions of transistors on a single die new problems arise on the way They are concerned with the minishing fabrication process features which a ect for example the gate to wire delay ratio They manifest themselves in greater variations of size and operating parameters of devices which put the overall reliability of systems at risk And most of all they have tremendous impact on design productivity where the costs of utilizing the growing silicon real estate rocket to billions of dollars that have to be spent on design veri cation and testing All such problems call for new sign approaches and models for digital systems Furthermore new developments in non CMOS technologies such as single electron transistors rapid single quantum devices quantum dot cells molecular devices etc add extra demand for new research in system design methodologies What kind of models and design methodologies will be required to build systems in all these new technologies Answering this question even for each particular type of new technology generation is not easy especially because sometimes it is not even clear what kind of elementary devices are feasible there This problem is of an interdisciplinary nature It requires an bridges between di erent scienti c communities The bridges must be built very quickly and be maximally exible to accommodate changes taking place in a logarithmic timescale   **Lectures on Concurrency and Petri Nets** Jörg Desel,Wolfgang Reisig,Grzegorz Rozenberg,2004-07-09 This tutorial volume originates from the 4th Advanced Course on Petri Nets ACPN 2003 held in Eichsttt Germany in September 2003 In addition to lectures given at ACPN 2003 additional chapters have been commissioned to give a well balanced presentation of the state of the art in the area This book will be useful as both a

reference for those working in the area as well as a study book for the reader who is interested in an up to date overview of research and development in concurrent and distributed systems of course readers specifically interested in theoretical or applicational aspects of Petri nets will appreciate the book as well **Hardware Design and Petri Nets** Alex Yakovlev,Alexandre Yakovlev,Luis Gomes,Luciano Lavagno,2000-02-29 Hardware Design and Petri Nets presents a summary of the state of the art in the applications of Petri nets to designing digital systems and circuits The area of hardware design has traditionally been a fertile field for research in concurrency and Petri nets Many new ideas about modelling and analysis of concurrent systems and Petri nets in particular originated in theory of asynchronous digital circuits Similarly the theory and practice of digital circuit design have always recognized Petri nets as a powerful and easy to understand modelling tool The ever growing demand in the electronic industry for design automation to build various types of computer based systems creates many opportunities for Petri nets to establish their role of a formal backbone in future tools for constructing systems that are increasingly becoming distributed concurrent and asynchronous Petri nets have already proved very effective in supporting algorithms for solving key problems in synthesis of hardware control circuits However since the front end to any realistic design flow in the future is likely to rely on more pragmatic Hardware Description Languages HDLs such as VHDL and Verilog it is crucial that Petri nets are well interfaced to such languages Hardware Design and Petri Nets is divided into five parts which cover aspects of behavioral modelling analysis and verification synthesis from Petri nets and STGs design environments based on high level Petri nets and HDLs and finally performance analysis using Petri nets Hardware Design and Petri Nets serves as an excellent reference source and may be used as a text for advanced courses on the subject

Transactions on Petri Nets and Other Models of Concurrency I Wil van der Aalst,Jonathan Billington,2008-12-18 This volume contains revised and extended versions of a selection of key papers from workshops held at the 28th International Conference on Applications and Theory of Petri Nets and Other Models of Concurrency which took place in Siedlce Poland June 2007 **Lectures on Petri Nets II: Applications** Wolfgang Reisig,Grzegorz Rozenberg,1998-11-04 The two volume set originates from the Advanced Course on Petri Nets held in Dagstuhl Germany in September 1996 beyond the lectures given there additional chapters have been commissioned to give a well balanced presentation of the state of the art in the area Together with its companion volume Lectures on Petri Nets I Basic Models this book is the actual reference for the area and addresses professionals students lecturers and researchers who are interested in systems design and would like to learn to use Petri nets familiar with subareas of the theory or its applications and wish to view the whole area interested in learning about recent results presented within a unified framework planning to apply Petri nets in practical situations interested in the relationship of Petri nets to other models of concurrent systems Advances in Petri Nets 1991 Helmut Plünnecke,Wolfgang Reisig,1991 The main idea behind the series of volumes Advances in Petri Nets is to present to the general computer science community recent results which are the most representative and significant for the

development of the area Thepapers for the volumes are drawn mainly from the annual International Conferences on Applications and Theory of Petri Nets Selected papers from the latest conference are independently refereed and revised and extended as necessary Some further papers submitted directly to the editor are included Advances in Petri Nets 1991 covers the 11th International Conference on Applications and Theory of Petri Nets held in Paris France in June 1991 The volume contains the Bibliography of Petri Nets 1990 prepared by H Pl nnecke and W Reisig with over 4000 entries PUBLISHER S WEBSITE **Application and Theory of Petri Nets and Concurrency** Luis Gomes,Robert Lorenz,2023-05-27 This book constitutes the proceedings of the 44th International Conference on Application and Theory of Petri Nets and Concurrency PETRI NETS 2023 which took place in Lisbon Portugal in June 2023 The 21 full papers included in this book were carefully reviewed and selected from 47 submissions They were organized in topical sections as follows Process mining semantics tools verification timed models model transformation The book also includes two invited talks in full paper length **Formal Aspects of Component Software** Farhad Arbab,Sung-Shik Jongmans,2020-02-12 This book constitutes the thoroughly revised selected papers from the 16th International Conference on Formal Aspects of Component Software FACS 2019 held in Amsterdam The Netherlands in October 2019 The 9 full papers presented together with 9 full papers and 3 short papers as well as 2 other papers were carefully reviewed and selected from 27 submissions FACS 2019 is concerned with how formal methods can be used to make component based and service oriented software development succeed Formal methods have provided a foundation for component based software by successfully addressing challenging issues such as mathematical models for components composition and adaptation or rigorous approaches to verification deployment testing and certification Prototyping of Concurrent Control Systems Implemented in FPGA Devices Remigiusz Wiśniewski,2016-09-30 This book focuses on prototyping aspects of concurrent control systems and their further implementation and partial reconfiguration in programmable devices Further it lays out a full prototyping flow for concurrent control systems Based on a given primary specification a system is described with an interpreted Petri net which naturally reflects the concurrent and sequential relationships of the design The book shows that apart from the traditional option of static configuration of the entire system the latest programmable devices especially FPGAs offer far more sophistication Partial reconfiguration allows selected parts of the system to be replaced without having to reprogram the entire structure of the device Approaches to dynamic and static partial reconfiguration of concurrent control systems are presented and described in detail p The theoretical work is illustrated by examples drawn from various applications with a milling machine and a traffic light controller highlighted as representative interpreted Petri nets Given the ubiquity of concurrent control systems in a huge variety of technological areas including transportation medicine artificial intelligence manufacturing security and safety and planetary exploration the innovative software and hardware design methods described here will be of considerable interest to control engineers and systems and circuits researchers in many areas of industry and academia **Advances in Petri**

Nets 1987 Grzegorz Rozenberg,1987-06-22 The aim of these series of volumes Advances in Petri Nets is to present to the general computer science community the most significant recent results with regard to the development in the area The main source of the papers are the annual European Workshops on Applications and Theory of Petri Nets the highest ranked papers from the past workshops are considered for the series i e they are again reviewed and accordingly revised or extended In addition to the workshop papers the Advances also present invited papers The present volume Advances in Petri Nets 1987 covers the 7th European Workshop on Applications and Theory of Petri Nets held in Oxford Great Britain in June 1986 It also contains a survey on complexity of problems related to Petri nets written by R R Howell and L E Rosier A special feature of this volume is a bibliography on Petri nets containing more than 2000 entries   Hardware Design and Petri Nets Alex Yakovlev,Luis Gomes,Luciano Lavagno,2013-04-17 Hardware Design and Petri Nets presents a summary of the state of the art in the applications of Petri nets to designing digital systems and circuits The area of hardware design has traditionally been a fertile field for research in concurrency and Petri nets Many new ideas about modelling and analysis of concurrent systems and Petri nets in particular originated in theory of asynchronous digital circuits Similarly the theory and practice of digital circuit design have always recognized Petri nets as a powerful and easy to understand modelling tool The ever growing demand in the electronic industry for design automation to build various types of computer based systems creates many opportunities for Petri nets to establish their role of a formal backbone in future tools for constructing systems that are increasingly becoming distributed concurrent and asynchronous Petri nets have already proved very effective in supporting algorithms for solving key problems in synthesis of hardware control circuits However since the front end to any realistic design flow in the future is likely to rely on more pragmatic Hardware Description Languages HDLs such as VHDL and Verilog it is crucial that Petri nets are well interfaced to such languages Hardware Design and Petri Nets is divided into five parts which cover aspects of behavioral modelling analysis and verification synthesis from Petri nets and STGs design environments based on high level Petri nets and HDLs and finally performance analysis using Petri nets Hardware Design and Petri Nets serves as an excellent reference source and may be used as a text for advanced courses on the subject

FME 2003: Formal Methods Keijiro Araki,2003-08-27 This book constitutes the refereed proceedings of the International Symposium of Formal Methods Europe FME 2003 held in Pisa Italy in September 2003 The 44 revised full papers presented together with 5 invited papers were carefully reviewed and selected from 144 submissions The papers are organized in topical sections on industrial issues control systems and applications communication system verfication co specification and compilers composition Java object orientation and modularity model checking parallel processes program checking and testing B method and security   *Formal Methods in Computer-Aided Design* Mark D. Aagaard,John W. O'Leary,2003-06-30 This volume contains the proceedings of the Fourth Biennial Conference on F mal Methods in Computer Aided Design FMCAD The conference is devoted to the use of mathematical methods for the analysis of digital hardware c cuits and

systems The workreported in this bookdescribes the use of formal mathematics and associated tools to design and verify digital hardware systems Functional veri cation has become one of the principal costs in a modern computer design e ort FMCAD provides a venue for academic and industrial researchers and practitioners to share their ideas and experiences of using screte mathematical modeling and veri cation Over the past 20 years this area has grown from just a few academic researchers to a vibrant worldwide com nity of people from both academia and industry This volume includes 23 papers selected from the 47 submitted papers each of which was reviewed by at least three program committee members The history of FMCAD dates backto 1984 when the earliest meetings on this topic occurred as part of IFIP WG10 2 **Iaeng Transactions On Engineering Technologies Volume 7 - Special Edition Of The International Multiconference Of Engineers And Computer Scientists 2011** Sio-iong Ao,Alan Hoi-shou Chan,Hideki Katagiri,Li Xu,2012-01-27 This volume contains revised and extended research articles by prominent researchers Topics covered include operations research scientific computing industrial engineering electrical engineering communication systems and industrial applications The book offers the state of the art advances in engineering technologies and also serves as an excellent reference work for researchers and graduate students working with on engineering technologies a **IAENG Transactions on Engineering Technologies** Sio-Iong Ao,2012 This volume contains revised and extended research articles by prominent researchers Topics covered include operations research scientific computing industrial engineering electrical engineering communication systems and industrial applications The book offers the state of the art advances in engineering technologies and also serves as an excellent reference work for researchers and graduate students working with on engineering technologies

Advances in Petri Nets ,1991 *Application and Theory of Petri Nets 2000* Mogens Nielsen,Dan Simpson,2003-06-26 This book constitutes the refereed proceedings of the 21st International Conference on Application and Theory of Petri Nets ICATPN 2000 held in Aarhus Denmark in June 2000 The 20 revised full papers presented together with four invited surveys and four tool presentations were carefully reviewed and selected from 57 submissions The papers address all current aspects of Petri net research and development including system design and verification UML compositionality process algebras model checking computer networking business process engineering communication networks etc Various classes of Petri nets are discussed including safe Petri nets high level Petri nets colored Petri nets P T nets and timed Petri nets

Application and Theory of Petri Nets and Concurrency Luca Bernardinello,Laure Petrucci,2022-06-13 This book constitutes the proceedings of the 43rd International Conference on Application and Theory of Petri Nets and Concurrency PETRI NETS 2022 which was held virtually in June 2021 The 19 full papers presented in this volume were carefully reviewed and selected from 35 submissions The papers are categorized into the following topical sub headings application of concurrency to system design timed models tools applications synthesis petri nets architecture and process mining

Current Trends in Theoretical Computer Science Gheorghe P?un,2004 This book is based on columns and tutorials

published in the Bulletin of the European Association for Theoretical Computer Science EATCS during the period 2000OCo2003 It presents many of the most active current research lines in theoretical computer science The material appears in two volumes OC Algorithms and ComplexityOCO and OC Formal Models and SemanticsOCO reflecting the traditional division of the field The list of contributors includes many of the well known researchers in theoretical computer science Most of the articles are reader friendly and do not presuppose much knowledge of the area in question Therefore the book constitutes very suitable supplementary reading material for various courses and seminars in computer science Contents Vol 1 Algorithms Computational Complexity Distributed Computing Natural Computing Vol 2 Formal Specification Logic in Computer Science Concurrency Formal Language Theory Readership Upper level undergraduates graduate students and researchers in theoretical computer science and biocomputing

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