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IEEE Electron Devices Society

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Vadim Issakov



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Radio Frequency Integrated Circuit Design John W. M. Rogers, Calvin Plett, 2010 This newly revised and expanded edition of the 2003 Artech House classic Radio Frequency Integrated Circuit Design serves as an up to date practical reference for complete RFIC know how The second edition includes numerous updates including greater coverage of CMOS PA design RFIC design with on chip components and more worked examples with simulation results By emphasizing working designs this book practically transports you into the authors own RFIC lab so you can fully understand the function of each design detailed in this book Among the RFIC designs examined are RF integrated LC based filters VCO automatic amplitude control loops and fully integrated transformer based circuits as well as image reject mixers and power amplifiers If you are new to RFIC design you can benefit from the introduction to basic theory so you can quickly come up to speed on how RFICs perform and work together in a communications device A thorough examination of RFIC technology guides you in knowing when RFICs are the right choice for designing a communication device This leading edge resource is packed with over 1 000 equations and more than 435 illustrations that support key topics

Radio Frequency Integrated Circuits and Systems Hooman Darabi, 2020-03-12 Equips students with essential industry relevant knowledge through in depth explanations practical applications examples and exercises

1998 IEEE Radio Frequency Integrated Circuits (RFIC) Symposium, 1998
Radio-Frequency Integrated-Circuit Engineering Cam Nguyen, 2015-03-16 Radio Frequency Integrated Circuit Engineering addresses the theory analysis and design of passive and active RFIC s using Si based CMOS and Bi CMOS technologies and other non silicon based technologies The materials covered are self contained and presented in such detail that allows readers with only undergraduate electrical engineering knowledge in EM RF and circuits to understand and design RFICs Organized into sixteen chapters blending analog and microwave engineering Radio Frequency Integrated Circuit Engineering emphasizes the microwave engineering approach for RFICs Provides essential knowledge in EM and microwave engineering passive and active RFICs RFIC analysis and design techniques and RF systems vital for RFIC students and engineers Blends analog and microwave engineering approaches for RFIC design at high frequencies Includes problems at the end of each chapter

1998 IEEE Radio Frequency Integrated Circuits (RFIC) Symposium IEEE Microwave Theory and Techniques Society, 1998
Radio Frequency Integrated Circuit Design for Cognitive Radio Systems Amr Fahim, 2015-03-03 This book fills an information gap on cognitive radios since the discussion focuses on the implementation issues that are unique to cognitive radios and how to solve them at both the architecture and circuit levels This is the first book to describe in detail cognitive radio systems as well as the circuit implementation and architectures required to implement such systems Throughout the book requirements and constraints imposed by cognitive radio systems are emphasized when discussing the circuit implementation details This is a valuable reference for anybody with background in analog and radio frequency RF integrated circuit design needing to learn more about integrated circuits requirements and

implementation for cognitive radio systems *High-Linearity CMOS RF Front-End Circuits* Yongwang Ding, Ramesh Harjani, 2006-02-08 This book focuses on high performance radio frequency integrated circuits RF IC design in CMOS 1 Development of radio frequency ICs Wireless communications has been advancing rapidly in the past two decades Many high performance systems have been developed such as cellular systems AMPS GSM TDMA CDMA W CDMA etc GPS system global positioning system and WLAN wireless local area network systems The rapid growth of VLSI technology in both digital circuits and analog circuits provides benefits for wireless communication systems Twenty years ago not many people could imagine millions of transistors in a single chip or a complete radio for size of a penny Now not only complete radios have been put in a single chip but also more and more functions have been realized by a single chip and at a much lower price A radio transmits and receives electro magnetic signals through the air The signals are usually transmitted on high frequency carriers For example a typical voice signal requires only 30 KiloHertz bandwidth When it is transmitted by a FM radio station it is often carried by a frequency in the range of tens of megahertz to hundreds of megahertz Usually a radio is categorized by its carrier frequency such as 900 MHz radio or 5 GHz radio In general the higher the carrier frequency the better the directivity but the more difficult the radio design **Radio Frequency Integrated Circuits and Technologies** Frank Ellinger, 2008-09-11 In the last decade wireless communications engineering has seen outstanding progress making merged enhanced and novel applications in the area of mobile phones wireless networks sensors and television feasible Technologies have developed from hybrid systems to highly integrated solutions in silicon SiGe GaAs and InP By aggressive scaling of device dimensions below 0.1 μm and employing advanced technologies such as SOI strained silicon and low k circuits with operation frequencies and bandwidths up to approximately 100 GHz can now be fabricated However especially in silicon the restrictions inherent in scaling make circuit engineering a demanding task Examples of these drawbacks are the limited high frequency signal power leakage effects and significant parasitics in passive devices Enhanced circuit topologies and design techniques have to be applied to achieve maximum performance In this context designers must have profound skills in the following areas circuit theory IC technologies communications standards system design measurement techniques etc The aim of this book is to address all these multidisciplinary issues in a compact and comprehensive form and in a single volume Suitable for students engineers and scientists the manuscript provides the necessary theoretical background together with cookbook like optimisation strategies and state of the art design examples Each chapter is accompanied by tutorial questions repeating the key issues of the treated subjects The manuscript is organised as follows Chapter 1 preludes with an introduction concerned with the exciting history of integrated circuits technologies and wireless communications **RF Electronics for Electronic Warfare** Richard A. Poisel, 2019-08-31 This exciting new resource investigates the function of RF communication in electronic warfare systems The book provides in depth coverage of how RF signals must be constructed to perform jamming missions which prevent a receiver from properly extracting a target signal Technical descriptions of

oscillators and modulators which generate the RF signals are presented and explored Power supplies that generate adequate power for fueling high power amplifiers are also described and their operations investigated Oscillator basics including principles of oscillator operation phase locked loop synthesizers and direct digital synthesis are examined Fundamentals of RF communications including power supplies for RF power amplifiers are included making it useful for both novice and advanced practitioners Written by a prominent expert in the field this authoritative book is the first available that combines the topics of electronic warfare and oscillator design and analysis

RF and Microwave Passive and Active Technologies
Mike Golio, Janet Golio, 2018-10-03 In the high frequency world the passive technologies required to realize RF and microwave functionality present distinctive challenges SAW filters dielectric resonators MEMS and waveguide do not have counterparts in the low frequency or digital environment Even when conventional lumped components can be used in high frequency applications their behavior does not resemble that observed at lower frequencies RF and Microwave Passive and Active Technologies provides detailed information about a wide range of component technologies used in modern RF and microwave systems Updated chapters include new material on such technologies as MEMS device packaging surface acoustic wave SAW filters bipolar junction and heterojunction transistors and high mobility electron transistors HMETs The book also features a completely rewritten section on wide bandgap transistors

CMOS PLL Synthesizers: Analysis and Design Keliu Shu, Edgar Sanchez-Sinencio, 2006-01-20 Thanks to the advance of semiconductor and communication technology the wireless communication market has been booming in the last two decades It evolved from simple pagers to emerging third generation 3G cellular phones In the meanwhile broadband communication market has also gained a rapid growth As the market always demands hi performance and low cost products circuit designers are seeking hi integration communication devices in cheap CMOS technology The phase locked loop frequency synthesizer is a critical component in communication devices It works as a local oscillator for frequency translation and channel selection in wireless transceivers and broadband cable tuners It also plays an important role as the clock synthesizer for data converters in the analog and digital signal interface This book covers the design and analysis of PLL synthesizers It includes both fundamentals and a review of the state of the art techniques The transient analysis of the third order charge pump PLL reveals its locking behavior accurately The behavioral level simulation of PLL further clarifies its stability limit Design examples are given to clearly illustrate the design procedure of PLL synthesizers A complete derivation of reference spurs in the charge pump PLL is also presented in this book The in depth investigation of the digital CA modulator for fractional N synthesizers provides insightful design guidelines for this important block

Generalized Low-Voltage Circuit Techniques for Very High-Speed Time-Interleaved Analog-to-Digital Converters Sai-Weng Sin, Seng-Pan U, Rui Paulo Martins, 2010-09-29 Analog to Digital Converters ADCs play an important role in most modern signal processing and wireless communication systems where extensive signal manipulation is necessary to be performed by complicated digital signal processing DSP circuitry This trend

also creates the possibility of fabricating all functional blocks of a system in a single chip System On Chip SoC with great reductions in cost chip area and power consumption However this tendency places an increasing challenge in terms of speed resolution power consumption and noise performance in the design of the front end ADC which is usually the bottleneck of the whole system especially under the unavoidable low supply voltage imposed by technology scaling as well as the requirement of battery operated portable devices Generalized Low Voltage Circuit Techniques for Very High Speed Time Interleaved Analog to Digital Converters will present new techniques tailored for low voltage and high speed Switched Capacitor SC ADC with various design specific considerations

The VLSI Handbook Wai-Kai Chen, 2019-07-17 Over the years the fundamentals of VLSI technology have evolved to include a wide range of topics and a broad range of practices To encompass such a vast amount of knowledge The VLSI Handbook focuses on the key concepts models and equations that enable the electrical engineer to analyze design and predict the behavior of very large scale integrated circuits It provides the most up to date information on IC technology you can find Using frequent examples the Handbook stresses the fundamental theory behind professional applications Focusing not only on the traditional design methods it contains all relevant sources of information and tools to assist you in performing your job This includes software databases standards seminars conferences and more The VLSI Handbook answers all your needs in one comprehensive volume at a level that will enlighten and refresh the knowledge of experienced engineers and educate the novice This one source reference keeps you current on new techniques and procedures and serves as a review for standard practice It will be your first choice when looking for a solution

Low Power RF Circuit Design in Standard CMOS Technology Unai Alvarado, Guillermo Bistué, Iñigo Adín, 2011-10-18 Low Power Consumption is one of the critical issues in the performance of small battery powered handheld devices Mobile terminals feature an ever increasing number of wireless communication alternatives including GPS Bluetooth GSM 3G WiFi or DVB H Considering that the total power available for each terminal is limited by the relatively slow increase in battery performance expected in the near future the need for efficient circuits is now critical This book presents the basic techniques available to design low power RF CMOS analogue circuits It gives circuit designers a complete guide of alternatives to optimize power consumption and explains the application of these rules in the most common RF building blocks LNA mixers and PLLs It is set out using practical examples and offers a unique perspective as it targets designers working within the standard CMOS process and all the limitations inherent in these technologies

Microwave and Millimetre-Wave Design for Wireless Communications Ian Robertson, Nutapong Somjit, Mitchai Chongcheawchamnan, 2016-08-29 This book describes a full range of contemporary techniques for the design of transmitters and receivers for communications systems operating in the range from 1 through to 300 GHz In this frequency range there is a wide range of technologies that need to be employed with silicon ICs at the core but compared with other electronics systems a much greater use of more specialist devices and components for high performance for example high Q factor low

loss and good power efficiency Many text books do of course cover these topics but what makes this book timely is the rapid adoption of millimetre waves frequencies from 30 to 300 GHz for a wide range of consumer applications such as wireless high definition TV 5G Gigabit mobile internet systems and automotive radars It has taken many years to develop low cost technologies for suitable transmitters and receivers so previously these frequencies have been employed only in expensive military and space applications The book will cover these modern technologies with the follow topics covered transmitters and receivers lumped element filters transmission lines and S parameters RF MEMS RFICs and MMICs and many others In addition the book includes extensive line diagrams to illustrate circuit diagrams and block diagrams of systems including diagrams and photographs showing how circuits are implemented practically Furthermore case studies are also included to explain the salient features of a range of important wireless communications systems The book is accompanied with suitable design examples and exercises based on the Advanced Design System the industry leading CAD tool for wireless design More importantly the authors have been working with Keysight Technologies on a learning teaching initiative which is designed to promote access to industry standard EDA tools such as ADS Through its University Educational Support Program Keysight offers students the opportunity to request a student license backed up with extensive classroom materials and support resources This culminates with students having the chance to demonstrate their RF MW design and measurement expertise through the Keysight RF Microwave Industry Ready Student Certification Program www.keysight.com find eesof university www.keysight.com find eesof student certification

Design of High-Performance CMOS Voltage-Controlled Oscillators Liang Dai, Ramesh Harjani, 2012-12-06 Design of High Performance CMOS Voltage Controlled Oscillators presents a phase noise modeling framework for CMOS ring oscillators The analysis considers both linear and nonlinear operation It indicates that fast rail to rail switching has to be achieved to minimize phase noise Additionally in conventional design the flicker noise in the bias circuit can potentially dominate the phase noise at low offset frequencies Therefore for narrow bandwidth PLLs noise up conversion for the bias circuits should be minimized We define the effective Q factor Q_{eff} for ring oscillators and predict its increase for CMOS processes with smaller feature sizes Our phase noise analysis is validated via simulation and measurement results The digital switching noise coupled through the power supply and substrate is usually the dominant source of clock jitter Improving the supply and substrate noise immunity of a PLL is a challenging job in hostile environments such as a microprocessor chip where millions of digital gates are present

Microwave Circuits for 24 GHz Automotive Radar in Silicon-based Technologies Vadim Issakov, 2010-08-05 There are continuous efforts focussed on improving road traffic safety worldwide Numerous vehicle safety features have been invented and standardized over the past decades Particularly interesting are the driver assistance systems since these can considerably reduce the number of accidents by supporting drivers perception of their surroundings Many driver assistance features rely on radar based sensors Nowadays the commercially available automotive front end sensors are comprised of

discrete components thus making the radar modules highly priced and suitable for integration only in premium class vehicles. Realization of low cost radar front end circuits would enable their implementation in inexpensive economy cars considerably contributing to traffic safety. Cost reduction requires high level integration of the microwave front end circuitry specifically analog and digital circuit blocks co located on a single chip. Recent developments of silicon based technologies e.g. CMOS and SiGe C bipolar make them suitable for realization of microwave sensors. Additionally these technologies offer the necessary integration capability. However the required output power and temperature stability necessary for automotive radar sensor products have not yet been achieved in standard digital CMOS technologies. On the other hand SiGe bipolar technology offers excellent high frequency characteristics and necessary output power for automotive applications but has lower potential for realization of digital blocks than CMOS.

Computer-Aided Design of Analog Integrated Circuits and Systems Rob A. Rutenbar, Georges G. E. Gielen, 2002-05-06 The tools and techniques you need to break the analog design bottleneck. Ten years ago analog seemed to be a dead end technology. Today System on Chip SoC designs are increasingly mixed signal designs. With the advent of application specific integrated circuits ASIC technologies that can integrate both analog and digital functions on a single chip analog has become more crucial than ever to the design process. Today designers are moving beyond hand crafted one transistor at a time methods. They are using new circuit and physical synthesis tools to design practical analog circuits, new modeling and analysis tools to allow rapid exploration of system level alternatives and new simulation tools to provide accurate answers for analog circuit behaviors and interactions that were considered impossible to handle only a few years ago. To give circuit designers and CAD professionals a better understanding of the history and the current state of the art in the field, this volume collects in one place the essential set of analog CAD papers that form the foundation of today's new analog design automation tools. Areas covered are: Analog synthesis, Symbolic analysis, Analog layout, Analog modeling and analysis, Specialized analog simulation, Circuit centering and yield optimization, Circuit testing. *Computer Aided Design of Analog Integrated Circuits and Systems* is the cutting edge reference that will be an invaluable resource for every semiconductor circuit designer and CAD professional who hopes to break the analog design bottleneck.

The RF and Microwave Handbook Mike Golio, 2000-12-20 The recent shift in focus from defense and government work to commercial wireless efforts has caused the job of the typical microwave engineer to change dramatically. The modern microwave and RF engineer is expected to know customer expectations, market trends, manufacturing technologies and factory models to a degree that is unprecedented in the field.

MicroNano Integration Harald Knobloch, Yvette Kaminorz, 2012-12-06 In recent years micro and nanotechnologies became key technologies having a significant effect on the development of new products and production technologies as well as on novel medical diagnosis and treatment methods. Moreover due to their enormous potential for new applications micro and nanotechnologies in future will increasingly affect our every day's life with an impact comparable to that of the industrial revolution in the 19th century or

the developments in microelectronics in the 20th century Although micro and nanotechnologies in general are regarded as two independent disciplines there is obviously an intense interaction between these two fascinating technologies The term MicroNano Integration in this context addresses the enormous potential of synergy effects provided by the combination and mutual integration of micro and nanotechnological elements In order to discuss the integration aspects of micro and nanotechnologies the International Forum on MicroNano Integration a conference held in Potsdam Germany on December 3 4 2003 brought together researchers from both communities from industries as well as from scientific institutions The contributions to this book reflect the discussions during the conference

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