



Power Semiconductor Devices and Ics: 9th International Symposium 1997

IEEE Electron Devices Society

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K Morrison



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ISPSD '97 ,1997 Proceedings of the ... International Symposium on Power Semiconductor Devices and ICs ,2004

Compound Semiconductor Power Transistors and Electrochemical Society. Meeting,1998 *Advanced High Voltage Power Device Concepts* B. Jayant Baliga,2011-09-21 The devices described in *Advanced MOS Gated Thyristor Concepts* are utilized in microelectronics production equipment in power transmission equipment and for very high power motor control in electric trains steel mills etc Advanced concepts that enable improving the performance of power thyristors are discussed here along with devices with blocking voltage capabilities of 5 000 V 10 000 V and 15 000 V Throughout the book analytical models are generated to allow a simple analysis of the structures and to obtain insight into the underlying physics The results of two dimensional simulations are provided to corroborate the analytical models and give greater insight into the device operation **Advances in Silicon Carbide Processing and Applications** Stephen E. Saddow,Anant K.

Agarwal,2004 Learn the latest advances in SiC Silicon Carbide technology from the leading experts in the field with this new cutting edge resource The book is your single source for in depth information on both SiC device fabrication and system level applications This comprehensive reference begins with an examination of how SiC is grown and how defects in SiC growth can affect working devices Key issues in selective doping of SiC via ion implantation are covered with special focus on implant conditions and electrical activation of implants SiC applications discussed include chemical sensors motor control components high temperature gas sensors and high temperature electronics By cutting through the arcane data and jargon surrounding the hype on SiC this book gives an honest assessment of today s SiC technology and shows you how SiC can be adopted in developing tomorrow s applications **Fundamentals of Power Semiconductor Devices** B. Jayant

Baliga,2010-04-02 *Fundamentals of Power Semiconductor Devices* provides an in depth treatment of the physics of operation of power semiconductor devices that are commonly used by the power electronics industry Analytical models for explaining the operation of all power semiconductor devices are shown The treatment here focuses on silicon devices but includes the unique attributes and design requirements for emerging silicon carbide devices The book will appeal to practicing engineers in the power semiconductor device community **Index to IEEE Publications** Institute of Electrical and Electronics

Engineers,1997 **Silicon Carbide Power Devices** B Jayant Baliga,2006-01-05 Power semiconductor devices are widely used for the control and management of electrical energy The improving performance of power devices has enabled cost reductions and efficiency increases resulting in lower fossil fuel usage and less environmental pollution This book provides the first cohesive treatment of the physics and design of silicon carbide power devices with an emphasis on unipolar structures It uses the results of extensive numerical simulations to elucidate the operating principles of these important devices [Power Electronics Handbook](#) Muhammad H. Rashid,2023-09-27 *Power Electronics Handbook Fifth Edition*

delivers an expert guide to power electronics and their applications The book examines the foundations of power electronics

power semiconductor devices and power converters before reviewing a constellation of modern applications

Comprehensively updated throughout this new edition features new sections addressing current practices for renewable energy storage transmission integration and operation as well as smart grid security intelligent energy artificial intelligence and machine learning applications applied to power electronics and autonomous and electric vehicles This handbook is aimed at practitioners and researchers undertaking projects requiring specialist design analysis installation commissioning and maintenance services Provides a fully comprehensive work addressing each aspect of power electronics in painstaking depth Delivers a methodical technical presentation in over 1500 pages Includes 50 contributions prepared by leading experts Offers practical support and guidance with detailed examples and applications for lab and field experimentation Includes new technical sections on smart grid security and intelligent energy artificial intelligence and machine learning applications applied to power electronics and autonomous and electric vehicles Features new chapter level templates and a narrative progression to facilitate understanding

The Electrical Engineering Handbook Wai Kai Chen, 2004-11-16 The Electrical Engineer's Handbook is an invaluable reference source for all practicing electrical engineers and students Encompassing 79 chapters this book is intended to enlighten and refresh knowledge of the practicing engineer or to help educate engineering students This text will most likely be the engineer's first choice in looking for a solution extensive complete references to other sources are provided throughout No other book has the breadth and depth of coverage available here This is a must have for all practitioners and students The Electrical Engineer's Handbook provides the most up to date information in Circuits and Networks Electric Power Systems Electronics Computer Aided Design and Optimization VLSI Systems Signal Processing Digital Systems and Computer Engineering Digital Communication and Communication Networks Electromagnetics and Control and Systems About the Editor in Chief Wai Kai Chen is Professor and Head Emeritus of the Department of Electrical Engineering and Computer Science at the University of Illinois at Chicago He has extensive experience in education and industry and is very active professionally in the fields of circuits and systems He was Editor in Chief of the IEEE Transactions on Circuits and Systems Series I and II President of the IEEE Circuits and Systems Society and is the Founding Editor and Editor in Chief of the Journal of Circuits Systems and Computers He is the recipient of the Golden Jubilee Medal the Education Award and the Meritorious Service Award from the IEEE Circuits and Systems Society and the Third Millennium Medal from the IEEE Professor Chen is a fellow of the IEEE and the American Association for the Advancement of Science 77 chapters encompass the entire field of electrical engineering THOUSANDS of valuable figures tables formulas and definitions Extensive bibliographic references

Electrical & Electronics Abstracts, 1997

Dual Mode Logic Itamar Levi, Alexander Fish, 2020-12-15 This book presents Dual Mode Logic DML a new design paradigm for digital integrated circuits DML logic gates can operate in two modes each optimized for a different metric Its on the fly switching between these operational modes at the gate block and system levels provide maximal E D optimization flexibility

Each highly detailed chapter has multiple illustrations showing how the DML paradigm seamlessly implements digital circuits that dissipate less energy while simultaneously improving performance and reducing area without a significant compromise in reliability. All the facets of the DML methodology are covered starting from basic concepts through single gate optimization, general module optimization, design trade-offs, and new ways DML can be integrated into standard design flows using standard EDA tools. DML logic is compatible with numerous applications but is particularly advantageous for ultra-low power, reliable, high performance systems and advanced scaled technologies. Written in language accessible to students and design engineers, each topic is oriented toward immediate application by all those interested in an alternative to CMOS logic.

Describes a novel promising alternative to conventional CMOS logic known as Dual Mode Logic (DML) with which a single gate can be operated selectively in two modes, each optimized for a different metric, e.g., energy consumption, performance, size.

Demonstrates several techniques at the architectural level which can result in high energy savings and improved system performance.

Focuses on the tradeoffs between power, area, and speed, including optimizations at the transistor and gate level, including alternatives to DML basic cells.

Illustrates DML efficiency for a variety of VLSI applications.

Wide Bandgap Semiconductor Power Devices B. Jayant Baliga, 2018-10-17

Wide Bandgap Semiconductor Power Devices: Materials, Physics, Design, and Applications provides readers with a single resource on why these devices are superior to existing silicon devices. The book lays the groundwork for an understanding of an array of applications and anticipated benefits in energy savings. Authored by the Founder of the Power Semiconductor Research Center at North Carolina State University and creator of the IGBT device, Dr. B. Jayant Baliga is one of the highest regarded experts in the field. He thus leads this team who comprehensively review the materials, device physics, design considerations, and relevant applications discussed.

Comprehensively covers power electronic devices including materials, both gallium nitride and silicon carbide, physics, design considerations, and the most promising applications.

Addresses the key challenges towards the realization of wide bandgap power electronic devices, including materials, defects, performance, and reliability.

Provides the benefits of wide bandgap semiconductors, including opportunities for cost reduction and social impact.

Advanced Power MOSFET Concepts B. Jayant Baliga, 2010-06-26

During the last decade, many new concepts have been proposed for improving the performance of power MOSFETs. The results of this research are dispersed in the technical literature among journal articles and abstracts of conferences. Consequently, the information is not readily available to researchers and practicing engineers in the power device community. There is no cohesive treatment of the ideas to provide an assessment of the relative merits of the ideas.

Advanced Power MOSFET Concepts provides an in-depth treatment of the physics of operation of advanced power MOSFETs. Analytical models for explaining the operation of all the advanced power MOSFETs will be developed. The results of numerical simulations will be provided to give additional insight into the device physics and validate the analytical models. The results of two-dimensional simulations will be provided to corroborate the analytical models and give greater insight into

the device operation Advanced Power Electronics Converters Euzeli dos Santos, Edison R. da Silva, 2014-11-24 This book covers power electronics in depth by presenting the basic principles and application details which can be used both as a textbook and reference book Introduces a new method to present power electronics converters called Power Blocks Geometry PBG Applicable for courses focusing on power electronics power electronics converters and advanced power converters Offers a comprehensive set of simulation results to help understand the circuits presented throughout the book

SOI Design Andrew Marshall, Sreedhar Natarajan, 2007-05-08 Silicon on insulator SOI is a very attractive technology for large volume integrated circuit production and is particularly good for low voltage low power and high speed digital systems SOI has also proved to be effective in various niche and growing markets IC processes based on SOI are known to reduce susceptibility to radiation and have been used for many years in high radiation environments SOI is also used for power integrated circuits micro electromechanical systems MEMS integrated optics and high temperature applications SOI offers numerous opportunities and challenges in the design of low voltage and low power CMOS circuits for both analog and digital applications The benefits of this technology for digital applications have been clear for many years The exploitation of SOI for analog and memory subsystems meanwhile has lagged behind digital developments but is now beginning to attain a level of parity with circuits that are in some cases improved over their bulk counterparts SOI is suitable for digital memory and analog designs although it is not necessarily straightforward to convert circuits developed for bulk processes into SOI Memory and most analog circuits either interface to or are incorporated within a digital environment The design of analog circuits on SOI in a mixed signal environment and memory design in an embedded memory application are discussed Various processes are examined and comparison is made between bulk and SOI circuit design concepts SOI is the process of choice in various RF applications particularly when digital circuitry is required SOI Design Analog Memory and Digital Techniques examines some of the basics but is primarily concerned with circuit related issues Static and dynamic logic circuit design has previously been studied in some detail however memory design for SOI and analog circuit designs have hitherto been examined only in a piecemeal manner SOI material is considered here in terms of implementation that are promising or have been used elsewhere in circuit development with historical perspective where appropriate SOI Design Analog Memory and Digital Techniques will be of interest to circuit design engineers It is also intended as a general graduate level text to introduce state of the art design principles for SOI circuit design **Insulated Gate Bipolar Transistor IGBT Theory and**

Design Vinod Kumar Khanna, 2004-03-22 A comprehensive and state of the art coverage of the design and fabrication of IGBT All in one resource Explains the fundamentals of MOS and bipolar physics Covers IGBT operation device and process design power modules and new IGBT structures The Art of Software Thermal Management for Embedded Systems Mark Benson, 2014-01-03 This book introduces Software Thermal Management STM as a means of reducing power consumption in a computing system in order to manage heat improve component reliability and increase system safety Readers will benefit

from this pragmatic guide to the field of STM for embedded systems and its catalog of software power management techniques Since thermal management is a key bottleneck in embedded systems design this book focuses on root cause of heat in embedded systems power Since software has an enormous impact on power consumption in an embedded system this book urges software engineers to manage heat effectively by understanding categorizing and developing new ways to reduce static and dynamic power consumption Whereas most books on thermal management describe mechanisms to remove heat this book focuses on ways for software engineers to avoid generating heat in the first place

Computer Arithmetic Earl E Swartzlander, 2015-03-17 This is the new edition of the classic book Computer Arithmetic in three volumes published originally in 1990 by IEEE Computer Society Press As in the original the book contains many classic papers treating advanced concepts in computer arithmetic which is very suitable as stand alone textbooks or complementary materials to textbooks on computer arithmetic for graduate students and research professionals interested in the field Told in the words of the initial developers this book conveys the excitement of the creators and the implementations provide insight into the details necessary to realize real chips This second volume presents topics on error tolerant arithmetic digit on line arithmetic number systems and now in this new edition a topic on implementations of arithmetic operations all wrapped with an updated overview and a new introduction for each chapter This volume is part of a 3 volume set Computer Arithmetic Volume I Computer Arithmetic Volume II Computer Arithmetic Volume III The full set is available for sale in a print only version

Contents Error Tolerant Arithmetic On Line Arithmetic VLSI Adder Implementations VLSI Multiplier Implementations Floating Point VLSI Chips Number Representation Implementations Readership Graduate students and research professionals interested in computer arithmetic Key Features It reprints the classic papers It covers advanced arithmetic operations It does this in the words of the original creators Keywords Computer Arithmetic Fault Tolerant Arithmetic On Line Arithmetic Adder Implementations Multiplier Implementations Floating Point Chips Number Representation Implementations

Radiation Effects And Soft Errors In Integrated Circuits And Electronic Devices Ronald D Schrimpf, Daniel M Fleetwood, 2004-07-29 This book provides a detailed treatment of radiation effects in electronic devices including effects at the material device and circuit levels The emphasis is on transient effects caused by single ionizing particles single event effects and soft errors and effects produced by the cumulative energy deposited by the radiation total ionizing dose effects Bipolar Si and SiGe metal oxide semiconductor MOS and compound semiconductor technologies are discussed In addition to considering the specific issues associated with high performance devices and technologies the book includes the background material necessary for understanding radiation effects at a more general level

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