2002 7th International Symposium on Plasma- and Process-Induced Damage

Terence Hook, Koji Eriguchi, and Calvin T. Gabriel
Editors

June 5-7, 2002 Maui, Hawaii, USA

Technical Co-Sponsors AVS IEEE/Electron Devices Society Japanese Society of Applied Physics

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Xiang Xie

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Plasma Process-Induced Damage, 1999 4th International Symposium on ,1999 **CMOS Plasma and Process** Damage Kirk Prall,2025-05-16 This book discusses the complex technology of building CMOS computer chips and covers some of the unusual problems that can occur during chip manufacturing Readers will learn how plasma and process damage results from the high energy processes that are used in chip manufacturing causing harm to the chips functional failure and Plasma Etching Processes for Sub-guarter Micron Devices G. S. Mathad, 2000 reliability problems International Symposium on Plasma Process-Induced Damage Oyo Butsuri Gakkai, 2000 This text covers topics such as CVD process damage effects electron shading mechanism front end process damage effects damage in multilevel interconnects damage effects characterization 300mm technology thin dielectrics and degradation mechanisms and antenna test structures Glow Discharge Polymerization Vladislav Zyn, 2024-12-09 The countless applications of plasma polymerization and design from housekeeping to nanotechnology and the generally weak understanding of the mechanisms of this process dictate the need to study its theoretical foundations This book comprises a detailed description of the formation of local mechanisms of polymer synthesis in the main zones of glow discharge by electrophysical collision type processes specific to each zone The zonal mechanisms are so different that the local polymerization rates can differ by hundreds of times The most powerful cathode mechanism creates polymer films with signs of radiation damage to the structure abnormal over compaction and associated compression stresses microcraters from ion shocks and micro explosions The book will be of interest to researchers postgraduates and students in plasma chemistry and plasma technology **Semiconductor Process** Reliability in Practice Zhenghao Gan, Waisum Wong, Juin Liou, 2012-10-10 Proven processes for ensuring semiconductor device reliability Co written by experts in the field Semiconductor Process Reliability in Practice contains detailed descriptions and analyses of reliability and qualification for semiconductor device manufacturing and discusses the underlying physics and theory The book covers initial specification definition test structure design analysis of test structure data and final qualification of the process Real world examples of test structure designs to qualify front end of line devices and back end of line interconnects are provided in this practical comprehensive guide Coverage includes Basic device physics Process flow for MOS manufacturing Measurements useful for device reliability characterization Hot carrier injection Gate oxide integrity GOI and time dependent dielectric breakdown TDDB Negative bias temperature instability Plasma induced damage Electrostatic discharge protection of integrated circuits Electromigration Stress migration Intermetal dielectric Plasma Charging Damage Kin P. Cheung, 2012-12-06 In the 50 years since the invention of transistor breakdown silicon integrated circuit IC technology has made astonishing advances A key factor that makes these advances possible is the ability to have precise control on material properties and physical dimensions. The introduction of plasma processing in pattern transfer and in thin film deposition is a critical enabling advance among other things In state of the art silicon Ie

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