

A photograph of the Golden Gate Bridge in San Francisco at dusk. The bridge's orange-red towers and suspension cables are visible against a dark, cloudy sky. The water below is a deep blue, and the city lights are visible in the distance. The text is overlaid on the right side of the image.

2025 IEEE International Solid-State Circuits Conference (ISSCC)

February 16-20, 2025 San Francisco, CA

1997 Ieee International Solid State Circuits Conference

João Goes, João C. Vital, José E. Franca



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IEEE International Solid-State Circuits Conference Digest of Technical Papers, 1997 IEEE Solid-State Circuits

Council, 1997-03 Design, Simulation and Applications of Inductors and Transformers for Si RF ICs Ali M. Niknejad, Robert G. Meyer, 2005-12-15 The modern wireless communication industry has put great demands on circuit designers for smaller cheaper transceivers in the gigahertz frequency range One tool which has assisted designers in satisfying these requirements is the use of on chip inductive elements inductors and transformers in silicon Si radio frequency RF integrated circuits ICs These elements allow greatly improved levels of performance in Si monolithic low noise amplifiers power amplifiers up conversion and down conversion mixers and local oscillators Inductors can be used to improve the intermodulation distortion performance and noise figure of small signal amplifiers and mixers In addition the gain of amplifier stages can be enhanced and the realization of low cost on chip local oscillators with good phase noise characteristics is made feasible In order to reap these benefits it is essential that the IC designer be able to predict and optimize the characteristics of on chip inductive elements Accurate knowledge of inductance values quality factor Q and the influence of adjacent elements on chip proximity effects and substrate losses is essential In this book the analysis modeling and application of on chip inductive elements is considered Using analyses based on Maxwells equations an accurate and efficient technique is developed to model these elements over a wide frequency range Energy loss to the conductive substrate is modeled through several mechanisms including electrically induced displacement and conductive currents and by magnetically induced eddy currents These techniques have been compiled in a user friendly software tool ASITIC Analysis and Simulation of Inductors and Transformers for Integrated Circuits **Intuitive Analog Circuit Design** Marc Thompson, 2013-11-12 Intuitive Analog Circuit Design outlines ways of thinking about analog circuits and systems that let you develop a feel for what a good working analog circuit design should be This book reflects author Marc Thompson's 30 years of experience designing analog and power electronics circuits and teaching graduate level analog circuit design and is the ideal reference for anyone who needs a straightforward introduction to the subject In this book Dr Thompson describes intuitive and back of the envelope techniques for designing and analyzing analog circuits including transistor amplifiers CMOS JFET and bipolar transistor switching noise in analog circuits thermal circuit design magnetic circuit design and control systems The application of some simple rules of thumb and design techniques is the first step in developing an intuitive understanding of the behavior of complex electrical systems Introducing analog circuit design with a minimum of mathematics this book uses numerous real world examples to help you make the transition to analog design The second edition is an ideal introductory text for anyone new to the area of analog circuit design LTSPICE files and PowerPoint files available online to assist readers and instructors in simulating circuits found in the text Design examples are used throughout the text along with end of chapter examples

Covers real world parasitic elements in circuit design and their effects

Digital System Clocking Vojin G.

Oklobdzija, Vladimir M. Stojanovic, Dejan M. Markovic, Nikola M. Nedovic, 2005-02-25 Provides the only up to date source on the most recent advances in this often complex and fascinating topic The only book to be entirely devoted to clocking Clocking has become one of the most important topics in the field of digital system design A must have book for advanced circuit engineers

Data Converters Franco Maloberti, 2007-05-15 This book is the first graduate level textbook presenting a comprehensive treatment of Data Converters It provides comprehensive definition of the parameters used to specify data converters and covers all the architectures used in Nyquist rate data converters The book uses Simulink and Matlab extensively in examples and problem sets This is a textbook that is also essential for engineering professionals as it was written in response to a shortage of organically organized material on the topic The book assumes a solid background in analog and digital circuits as well as a working knowledge of simulation tools for circuit and behavioral analysis

Silicon-on-Insulator Technology: Materials to VLSI J.-P. Colinge, 2012-12-06 Silicon on Insulator Technology Materials to VLSI Third Edition retraces the evolution of SOI materials devices and circuits over a period of roughly twenty years Twenty years of progress research and development during which SOI material fabrication techniques have been born and abandoned devices have been invented and forgotten but most importantly twenty years during which SOI Technology has little by little proven it could outperform bulk silicon in every possible way The turn of the century turned out to be a milestone for the semiconductor industry as high quality SOI wafers suddenly became available in large quantities From then on it took only a few years to witness the use of SOI technology in a wealth of applications ranging from audio amplifiers and wristwatches to 64 bit microprocessors This book presents a complete and state of the art review of SOI materials devices and circuits SOI fabrication and characterization techniques SOI CMOS processing and the physics of the SOI MOSFET receive an in depth analysis

CMOS Integrated Switching Power Converters Gerard Villar Piqué, Eduard Alarcón, 2011-05-20 This book describes the structured design and optimization of efficient energy processing integrated circuits The approach is multidisciplinary covering the monolithic integration of IC design techniques power electronics and control theory In particular this book enables readers to conceive synthesize design and implement integrated circuits with high density high efficiency on chip switching power regulators Topics covered encompass the structured design of the on chip power supply efficiency optimization IC compatible power inductors and capacitors power MOSFET switches and efficient switch drivers in standard CMOS technologies

NAND Flash Memory Technologies Seiichi

Aritome, 2015-12-29 Offers a comprehensive overview of NAND flash memories with insights into NAND history technology challenges evolutions and perspectives Describes new program disturb issues data retention power consumption and possible solutions for the challenges of 3D NAND flash memory Written by an authority in NAND flash memory technology with over 25 years experience

Systematic Design for Optimisation of Pipelined ADCs João Goes, João C. Vital, José E.

Franca, 2006-04-18 **Systematic Design for Optimisation of Pipelined ADCs** proposes and develops new strategies methodologies and tools for designing low power and low area CMOS pipelined A/D converters. The task is tackled by following a scientifically consistent approach. First of all, the state of the art in pipeline A/D converters is analysed with a double purpose: a) to identify the best suited among different strategies reported in literature and taking into account the objectives pursued; b) to identify the drawbacks of these strategies as a basic first step to improve them. Then, the book proposes a top-down design approach for implementing high performance low power and low area CMOS pipelined A/D converters through the conception, development and implementation of self-calibrated techniques to extend the linearity of some critical stages in the architecture of pipelined ADCs. The detailed analysis and modelling of some major non-idealities that limit the physical realisation of pipelined ADCs and the proposal, development and implementation of design methodologies to support systematic design of optimised instances of these converters which combine maximum performance with minimum power dissipation and minimum area occupation. LIST: Several implementations together with consistent measured results are presented. In particular, a practical realisation of a low power 14-bit 5MS/s CMOS pipelined ADC with background analogue self-calibration is fully described. The proposed approach is fully in line with the best practice regarding the design of mixed-signal integrated circuits. On the one hand, drawbacks of currently existing solutions are overcome through innovative strategies; and on the other hand, the expert knowledge is packaged and made available for reusability by the community of circuit designers. Finally, feasibility of the strategies and the associated encapsulated knowledge is granted through experimental validation of working silicon. **Systematic Design for Optimisation of Pipelined ADCs** serves as an excellent reference for analogue design engineers, especially designers of low power CMOS A/D converters. The book may also be used as a text for advanced reading on the subject. **NIST Serial Holdings** National Institute of Standards and Technology (U.S.), 2002 **Closing the Gap Between ASIC & Custom** David Chinnery, Kurt William Keutzer, 2002 This book carefully details design tools and techniques for high performance ASIC design. Using these techniques, the performance of ASIC designs can be improved by two to three times. Important topics include: Improving performance through microarchitecture; Timing driven floorplanning; Controlling and exploiting clock skew; High performance latch-based design in an ASIC methodology; Automatically identifying and synthesizing complex logic gates; Automated cell sizing to increase performance and reduce power; Controlling process variation. These techniques are illustrated by designs running two to three times the speed of typical ASICs in the same process generation. **Distributed Intelligent Circuits And Systems** Balwinder Baj, Brij B Gupta, Shailendra Singh, Monirujjaman Khan, 2024-03-06 The main objective of this book is to provide insights into recent advances in distributed intelligent circuits systems and their applications. Distributed intelligence is the key enabler for innovations in machine-to-machine communications. The innovations are directed towards keeping existing algorithms as the base and developing new intelligent systems by employing smart technologies. Artificial

intelligence AI and more specifically deep learning DL are receiving significant attention in assisting doctors in the detection of disease patterns without much human intervention In agriculture robots automate slow repetitive and dull tasks allowing farmers to focus more on improving overall production yields The evolving trends point to the interface of artificial intelligence with machines being a factor in enhancing the decision making capabilities of smart machines This book provides relevant theoretical frameworks that include basic models algorithms circuit designs and the latest developments in experimental aspects in the field of distributed intelligence systems for industrial applications The challenges encountered in the development of models for distributed intelligence systems for environmental monitoring are mitigated with artificial intelligence machine learning and deep learning This book identifies challenges and helps in applying solutions in the development of advanced intelligent systems for environmental monitoring

High-Linearity CMOS RF Front-End Circuits Yongwang Ding, Ramesh Harjani, 2006-02-08 This book focuses on high performance radio frequency integrated circuits RF IC design in CMOS 1 Development of radio frequency ICs Wireless communications has been advancing rapidly in the past two decades Many high performance systems have been developed such as cellular systems AMPS GSM TDMA CDMA W CDMA etc GPS system global positioning system and WLAN wireless local area network systems The rapid growth of VLSI technology in both digital circuits and analog circuits provides benefits for wireless communication systems Twenty years ago not many people could imagine millions of transistors in a single chip or a complete radio for size of a penny Now not only complete radios have been put in a single chip but also more and more functions have been realized by a single chip and at a much lower price A radio transmits and receives electro magnetic signals through the air The signals are usually transmitted on high frequency carriers For example a typical voice signal requires only 30 Kilohertz bandwidth When it is transmitted by a FM radio station it is often carried by a frequency in the range of tens of megahertz to hundreds of megahertz Usually a radio is categorized by its carrier frequency such as 900 MHz radio or 5 GHz radio In general the higher the carrier frequency the better the directivity but the more difficult the radio design

Ultra Wideband Mohammad Abdul Matin, 2012-10-03 Ultra wideband UWB has advanced and merged as a technology and many more people are aware of the potential for this exciting technology The current UWB field is changing rapidly with new techniques and ideas where several issues are involved in developing the systems Among UWB system design the UWB RF transceiver and UWB antenna are the key components Recently a considerable amount of researches has been devoted to the development of the UWB RF transceiver and antenna for its enabling high data transmission rates and low power consumption Our book attempts to present current and emerging trends in research and development of UWB systems as well as future expectations

Substrate Noise Coupling in Mixed-Signal ASICs Stéphane Donnay, Georges Gielen, 2006-05-31 This book is the first in a series of three dedicated to advanced topics in Mixed Signal IC design methodologies It is one of the results achieved by the Mixed Signal Design Cluster an initiative launched in 1998 as part of the TARDIS project funded by the European

Commission within the ESPRIT IV Framework This initiative aims to promote the development of new design and test methodologies for Mixed Signal ICs and to accelerate their adoption by industrial users As Microelectronics evolves Mixed Signal techniques are gaining a significant importance due to the wide spread of applications where an analog front end is needed to drive a complex digital processing subsystem In this sense Analog and Mixed Signal circuits are recognized as a bottleneck for the market acceptance of Systems On Chip because of the inherent difficulties involved in the design and test of these circuits Specially problems arising from the use of a common substrate for analog and digital components are a main limiting factor The Mixed Signal Cluster has been formed by a group of 11 Research and Development projects plus a specific action to promote the dissemination of design methodologies techniques and supporting tools developed within the Cluster projects The whole action ending in July 2002 has been assigned an overall budget of more than 8 million EURO

SOI Circuit Design Concepts Kerry Bernstein, Norman J. Rohrer, 2007-09-18 This book first introduces SOI device physics and its fundamental idiosyncrasies It then walks the reader through realizations of these mechanisms which are observed in common high speed microprocessor designs The book also offers rules of thumb and comparisons to conventional bulk CMOS to guide implementation and describes a number of unique circuit topologies that SOI supports

Publications in Engineering, 1998

Minimizing Spurious Tones in Digital Delta-Sigma Modulators Kaveh Hosseini, Michael Peter Kennedy, 2011-06-25 This book describes several Digital Delta Sigma Modulator DDSM architectures including multi stage noise shaping MASH error feedback modulator EFM and single quantizer SQ DDSM modulators with a focus on predicting and maximizing their cycle lengths The authors aim to demystify an important aspect of these particular DDSM structures namely the existence of spurs resulting from the inherent periodicity of DDSMs with constant inputs Simulink and MATLAB models and code are presented in Chapters 2 5 to enable the reader to reproduce the results in this work and to explore further These examples will also be helpful for first time designers of DDSMs

High Performance Memory Testing R. Dean Adams, 2005-12-29 Are memory applications more critical than they have been in the past Yes but even more critical is the number of designs and the sheer number of bits on each design It is assured that catastrophes which were avoided in the past because memories were small will easily occur if the design and test engineers do not do their jobs very carefully High Performance Memory Testing Design Principles Fault Modeling and Self Test is based on the author's 20 years of experience in memory design memory reliability development and memory self test High Performance Memory Testing Design Principles Fault Modeling and Self Test is written for the professional and the researcher to help them understand the memories that are being tested

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