

Dedicated Digital Processors

by David H. Stangor, University of Illinois at Chicago

Dedicated Digital Processors Methods In Hardware Software Co Design

Marco Ceccarelli



Dedicated Digital Processors Methods In Hardware Software Co Design:

Dedicated Digital Processors F. Mayer-Lindenberg, 2004-04-02 The recent evolution of digital technology has resulted in the design of digital processors with increasingly complex capabilities The implementation of hardware software co design methodologies provides new opportunities for the development of low power high speed DSPs and processor networks Dedicated digital processors are digital processors with an application specific computational task Dedicated Digital Processors presents an integrated and accessible approach to digital processor design principles processes and implementations based upon the author s considerable experience in teaching digital systems design and digital signal processing Emphasis is placed on presentation of hardware software co design methods with examples and illustrations provided throughout the text System on a chip and embedded systems are described and examples of high speed real time processing are given Coverage of standard and emerging DSP architectures enable the reader to make an informed selection when undertaking their own designs Presents readers with the elementary building blocks for the design of digital hardware systems and processor networks Provides a unique evaluation of standard DSP architectures whilst providing up to date information on the latest architectures including the TI 55x and TigerSharc chip families and the Virtex FPGA field programmable gate array Introduces the concepts and methodologies for describing and designing hardware VHDL is presented and used to illustrate the design of a simple processor A practical overview of hardware software codesign with design techniques and considerations illustrated with examples of real world designs Fundamental reading for graduate and senior undergraduate students of computer and electronic engineering and Practicing engineers developing DSP applications

Co-Synthesis of Hardware and Software for Digital Embedded Systems Rajesh Kumar Gupta, 2012-12-06 Co Synthesis of Hardware and Software for Digital Embedded Systems with a Foreword written by Giovanni De Micheli presents techniques that are useful in building complex embedded systems These techniques provide a competitive advantage over purely hardware or software implementations of time constrained embedded systems Recent advances in chip level synthesis have made it possible to synthesize application specific circuits under strict timing constraints This work advances the state of the art by formulating the problem of system synthesis using both application specific as well as reprogrammable components such as off the shelf processors Timing constraints are used to determine what part of the system functionality must be delegated to dedicated application specific hardware while the rest is delegated to software that runs on the processor This co synthesis of hardware and software from behavioral specifications makes it possible to realize real time embedded systems using off the shelf parts and a relatively small amount of application specific circuitry that can be mapped to semi custom VLSI such as gate arrays The ability to perform detailed analysis of timing performance provides the opportunity of improving the system definition by creating better phototypes Co Synthesis of Hardware and Software for Digital Embedded Systems is of interest to CAD researchers and developers who want to branch off into the expanding field of hardware

software co design as well as to digital system designers who are interested in the present power and limitations of CAD techniques and their likely evolution

Readings in Hardware/Software Co-Design Giovanni De Micheli, Rolf Ernst, Wayne Wolf, 2002 This title serves as an introduction and reference for the field with the papers that have shaped the hardware software co design since its inception in the early 90s

Hardware/Software Co-Design for Data Flow Dominated Embedded Systems Ralf Niemann, 1998-10-31 Introduces different tasks of hardware software co design including system specification hardware software partitioning co synthesis and co simulation Summarizes and classifies co design tools and methods for these tasks and presents the co design tool COOL useful for solving co design tasks for the class of data flow dominated embedded systems Primary emphasis is on hardware software partitioning and the co synthesis phase and their coupling A mathematical formulation of the hardware software partitioning problem is given and several novel approaches are presented and compared for solving the partitioning problem Annotation copyrighted by Book News Inc Portland OR

Architecture and Design of Distributed Embedded Systems Bernd Kleinjohann, 2013-04-18 Due to the decreasing production costs of IT systems applications that had to be realised as expensive PCBs formerly can now be realised as a system on chip Furthermore low cost broadband communication media for wide area communication as well as for the realisation of local distributed systems are available Typically the market requires IT systems that realise a set of specific features for the end user in a given environment so called embedded systems Some examples for such embedded systems are control systems in cars airplanes houses or plants information and communication devices like digital TV mobile phones or autonomous systems like service or edutainment robots For the design of embedded systems the designer has to tackle three major aspects The application itself including the man machine interface The target architecture of the system including all functional and non functional constraints and the design methodology including modelling specification synthesis test and validation The last two points are a major focus of this book This book documents the high quality approaches and results that were presented at the International Workshop on Distributed and Parallel Embedded Systems DIPES 2000 which was sponsored by the International Federation for Information Processing IFIP and organised by IFIP working groups WG10 3 WG10 4 and WG10 5 The workshop took place on October 18 19 2000 in Schlo Eringerfeld near Paderborn Germany Architecture and Design of Distributed Embedded Systems is organised similar to the workshop Chapters 1 and 4 Methodology I and II deal with different modelling and specification paradigms and the corresponding design methodologies Generic system architectures for different classes of embedded systems are presented in Chapter 2 In Chapter 3 several design environments for the support of specific design methodologies are presented Problems concerning test and validation are discussed in Chapter 5 The last two chapters include distribution and communication aspects Chapter 6 and synthesis techniques for embedded systems Chapter 7 This book is essential reading for computer science researchers and application developers

Signal and Image Processing for Biometrics Jacob Scharcanski, Hugo Proença, Eliza

Du,2014-02-20 This volume offers a guide to the state of the art in the fast evolving field of biometric recognition to newcomers and experienced practitioners It is focused on the emerging strategies to perform biometric recognition under uncontrolled data acquisition conditions The mainstream research work in this field is presented in an organized manner so the reader can easily follow the trends that best suits her his interests in this growing field The book chapters cover the recent advances in less controlled covert data acquisition frameworks segmentation of poor quality biometric data biometric data quality assessment normalization of poor quality biometric data contactless biometric recognition strategies biometric recognition robustness data resolution illumination distance pose motion occlusions multispectral biometric recognition multimodal biometrics fusion at different levels high confidence automatic surveillance **Hardware/Software Co-Design** Giovanni DeMicheli,M.G. Sami,2013-11-11 Concurrent design or co design of hardware and software is extremely important for meeting design goals such as high performance that are the key to commercial competitiveness Hardware Software Co Design covers many aspects of the subject including methods and examples for designing 1 general purpose and embedded computing systems based on instruction set processors 2 telecommunication systems using general purpose digital signal processors as well as application specific instruction set processors 3 embedded control systems and applications to automotive electronics The book also surveys the areas of emulation and prototyping systems with field programmable gate array technologies hardware software synthesis and verification and industrial design trends Most contributions emphasize the design methodology the requirements and state of the art of computer aided co design tools together with current design examples **Reconfigurable Computing: Architectures, Tools, and Applications** Roger Woods,Katherine Compton,Christos Bourganis,Pedro C. Diniz,2008-08-29 Coverage in this proceedings volume includes DNA and string processing applications reconfigurable computing hardware and systems image processing run time behavior instruction set extension as well as random number generation and financial computation Embedded Computer Systems: Architectures, Modeling, and Simulation Koen Bertels,Nikitas Dimopoulos,Cristina Silvano,Stephan Wong,2009-07-21 This book constitutes the refereed proceedings of the 9th International Workshop on Architectures Modeling and Simulation SAMOS 2009 held on Samos Greece on July 20 23 2009 The 18 regular papers presented were carefully reviewed and selected from 52 submissions The papers are organized in topical sections on architectures for multimedia multi many cores architectures VLSI architectures design architecture modeling and exploration tools In addition there are 14 papers from three special sessions which were organized on topics of current interest instruction set customization reconfigurable computing and processor architectures and mastering cell BE and GPU execution platforms **Euro-Par 2024: Parallel Processing Workshops** Silvina Caino-Lores,Demetris Zeinalipour,Thaleia Dimitra Doudali,David E. Singh,Gracia Ester Martín Garzón,Leonel Sousa,Diego Andrade,Tommaso Cucinotta,Donato D'Ambrosio,Patrick Diehl,Manuel F. Dolz,Admela Jukan,Raffaele Montella,Matteo Nardelli,Marta Garcia-Gasulla,Sarah Neuwirth,2025-07-11 The two volume set LNCS 15385

15386 constitutes the proceedings of the workshops and associated events that were held in conjunction with the 30th European Conference on Parallel and Distributed Processing Euro Par 2024 which took place in Madrid Spain during August 26 30 2024 Overall the Euro Par Workshops received a total of 84 submissions of which 60 were accepted for presentation They stem from the following workshops The 1st European Workshop on Quantum Computing for High Performance Computing EUROQHPC 2024 The 19th Workshop on Virtualization in High Performance Cloud Computing VHPC 2024 The 1st Workshop in High Performance Computing in Physics PHYSHPC 2024 The 4th Workshop on Asynchronous Many Task Systems for Exascale AMTE 2024 The 3rd EuroHPC Workshop on Dynamic Resources in HPC DYNRESHPC 2024 The 22nd International Workshop on Algorithms Models and Tools for Parallel Computing on Heterogeneous Platforms HETEROPAR 2024 The 1st Workshop on Next Steps in IoT Edge Cloud Continuum Evolution Research and Practice IECCONT 2024 The 1st Workshop about High Performance e Science HIPES 2024 The 2nd International Workshop on Scalable Compute Continuum WSCC 2024 In addition the proceedings contain 14 poster and demo papers that have been accepted from 30 submissions and 18 contributions in the PhD Symposium track that were accepted from 22 submissions

Electronic System-Level HW/SW Co-Design of Heterogeneous Multi-Processor Embedded Systems Luigi Pomante, 2022-09-01 Modern electronic systems consist of a fairly heterogeneous set of components Today a single system can be constituted by a hardware platform frequently composed of a mix of analog and digital components and by several software application layers The hardware can include several heterogeneous microprocessors e g GPP DSP GPU etc dedicated ICs ASICs and or FPGAs memories a set of local connections between the system components and some interfaces between the system and the environment sensors actuators etc Therefore on the one hand multi processor embedded systems are capable of meeting the demand of processing power and flexibility of complex applications On the other hand such systems are very complex to design and optimize so that the design methodology plays a major role in determining the success of the products For these reasons to cope with the increasing system complexity the approaches typically used today are oriented towards co design methodologies working at the higher levels of abstraction Unfortunately such methodologies are typically customized for the specific application suffer of a lack of generality and still need a considerable effort when real size project are envisioned Therefore there is still the need for a general methodology able to support the designer during the high level steps of a co design flow enabling an effective design space exploration before tackling the low level steps and thus committing to the final technology This should prevent costly redesign loops In such a context the work described in this book composed of two parts aims at providing models methodologies and tools to support each step of the co design flow of embedded systems implemented by exploiting heterogeneous multi processor architectures mapped on distributed systems as well as fully integrated onto a single chip

VLSI: Integrated Systems on Silicon Ricardo A. Reis, Luc Claesen, 2013-06-05 This book contains the papers that have been presented at the ninth Very Large Scale Integrated Systems conference VLSI 97 that is organized biannually by IFIP

Working Group 10 5 It took place at Hotel Serra Azul in Gramado Brazil from 26 30 August 1997 Previous conferences have taken place in Edinburgh Trondheim Vancouver Munich Grenoble and Tokyo The papers in this book report on all aspects of importance to the design of the current and future integrated systems The current trend towards the realization of versatile Systems on a Chip require attention of embedded hardware software systems dedicated ASIC hardware sensors and actuators mixed analog digital design video and image processing low power battery operation and wireless communication The papers as presented in Jhis book have been organized in two tracks where one is dealing with VLSI System Design and Applications and the other presents VLSI Design Methods and CAD The following topics are addressed VLSI System Design and Applications Track VLSI for Video and Image Processing Microsystem and Mixed mode design Communication And Memory System Design Cow voltage Low power Analog Circuits High Speed Circuit Techniques Application Specific DSP Architectures VLSI Design Methods and CAD Track Specification and Simulation at System Level Synthesis and Technology Mapping CAD Techniques for Low Power Design Physical Design Issues in Sub micron Technologies Architectural Design and Synthesis Testing in Complex Mixed Analog and Digital Systems

System Design Automation Renate Merker, Wolfgang Schwarz, 2013-03-09 Design automation of electronic and hybrid systems is a steadily growing field of interest and a permanent challenge for researchers in Electronics Computer Engineering and Computer Science System Design Automation presents some recent results in design automation of different types of electronic and mechatronic systems It deals with various topics of design automation ranging from high level digital system synthesis through analogue and heterogeneous system analysis and design up to system modeling and simulation Design automation is treated from the aspects of its theoretical fundamentals its basic approach and its methods and tools Several application cases are presented in detail The book consists of three chapters High Level System Synthesis Digital Hardware Software Systems Here embedded systems distributed systems and processor arrays as well as hardware software codesign are treated Also three special application cases are discussed in detail Analog and Heterogeneous System Design System Approach and Methodology This chapter copes with the analysis and design of hybrid systems comprised of analog and digital electronic and mechanical components System Simulation and Evaluation Methods and Tools In this chapter object oriented Modelling analog system simulation including fault simulation parameter optimization and system validation are regarded The contents of the book are based on material presented at the Workshop System Design Automation SDA 2000 organised by the Sonderforschungsbereich 358 of the Deutsche Forschungsgemeinschaft at TU Dresden

Robot Manipulators Marco Ceccarelli, 2008-09-01 In this book we have grouped contributions in 28 chapters from several authors all around the world on the several aspects and challenges of research and applications of robots with the aim to show the recent advances and problems that still need to be considered for future improvements of robot success in worldwide frames Each chapter addresses a specific area of modeling design and application of robots but with an eye to give an integrated view of what make a robot a unique modern system for many

different uses and future potential applications Main attention has been focused on design issues as thought challenging for improving capabilities and further possibilities of robots for new and old applications as seen from today technologies and research programs Thus great attention has been addressed to control aspects that are strongly evolving also as function of the improvements in robot modeling sensors servo power systems and informatics But even other aspects are considered as of fundamental challenge both in design and use of robots with improved performance and capabilities like for example kinematic design dynamics vision integration

Advanced Techniques for Embedded Systems Design and Test Juan C. López,Román Hermida,Walter Geisselhardt,2013-03-09 As electronic technology reaches the point where complex systems can be integrated on a single chip and higher degrees of performance can be achieved at lower costs designers must devise new ways to undertake the laborious task of coping with the numerous and non trivial problems that arise during the conception of such systems On the other hand shorter design cycles so that electronic products can fit into shrinking market windows put companies and consequently designers under pressure in a race to obtain reliable products in the minimum period of time New methodologies supported by automation and abstraction have appeared which have been crucial in making it possible for system designers to take over the traditional electronic design process and embedded systems is one of the fields that these methodologies are mainly targeting The inherent complexity of these systems with hardware and software components that usually execute concurrently and the very tight cost and performance constraints make them specially suitable to introduce higher levels of abstraction and automation so as to allow the designer to better tackle the many problems that appear during their design *Advanced Techniques for Embedded Systems Design and Test* is a comprehensive book presenting recent developments in methodologies and tools for the specification synthesis verification and test of embedded systems characterized by the use of high level languages as a road to productivity Each specific part of the design process from specification through to test is looked at with a constant emphasis on behavioral methodologies *Advanced Techniques for Embedded Systems Design and Test* is essential reading for all researchers in the design and test communities as well as system designers and CAD tools developers

Advances in Computer Systems Architecture Thambipillai Srikanthan,Jingling Xue,Chip-Hong Chang,2005-10-19 On behalf of the ProgramCommittee we are pleased to present the proceedings of the 2005 Asia Paci c Computer Systems Architecture Conference ACSAC 2005 held in the beautiful and dynamic country of Singapore This conference was the tenth in its series one of the leading forums for sharing the emerging research ndings in this eld In consultation with the ACSAC Steering Committee we selected a member Program Committee This Program Committee represented a broad spectrum of research expertise to ensure a good balance of research areas stitutions and experience while maintaining the high quality of this conference series This year s committee was of the same size as last year but had 19 new faces We received a total of 173 submissions which is 14% more than last year Each paper was assigned to at least three and in some cases four ProgramC mittee members for review Wherever

necessary the committee members called upon the expertise of their colleagues to ensure the highest possible quality in the reviewing process As a result we received 415 reviews from the Program Committee members and their 105 co reviewers whose names are acknowledged in the proceedings The conference committee adopted a systematic blind review process to provide a fair assessment of all submissions In the end we accepted 65 papers on a broad range of topics giving an acceptance rate of 37.5% We are grateful to all the Program Committee members and the co reviewers for their efforts in completing the reviews within a tight schedule

Codesign Jerzy Rozenblit, Klaus Buchenrieder, 1995

Hardware/Software Co-Design and Co-Verification Jean-Michel Bergé, Oz Levia, Jacques Rouillard, 2013-03-09 Co Design is the set of emerging techniques which allows for the simultaneous design of Hardware and Software In many cases where the application is very demanding in terms of various performances time surface power consumption trade offs between dedicated hardware and dedicated software are becoming increasingly difficult to decide upon in the early stages of a design Verification techniques such as simulation or proof techniques that have proven necessary in the hardware design must be dramatically adapted to the simultaneous verification of Software and Hardware Describing the latest tools available for both Co Design and Co Verification of systems Hardware Software Co Design and Co Verification offers a complete look at this evolving set of procedures for CAD environments The book considers all trade offs that have to be made when co designing a system Several models are presented for determining the optimum solution to any co design problem including partitioning architecture synthesis and code generation When deciding on trade offs one of the main factors to be considered is the flow of communication especially to and from the outside world This involves the modeling of communication protocols An approach to the synthesis of interface circuits in the context of co design is presented Other chapters present a co design oriented flexible component data base and retrieval methods a case study of an ethernet bridge designed using LOTOS and co design methodologies and finally a programmable user interface based on monitors Hardware Software Co Design and Co Verification will help designers and researchers to understand these latest techniques in system design and as such will be of interest to all involved in embedded system design

Neural Networks for Instrumentation, Measurement and Related Industrial Applications Sergey Ablameyko, 2003 This work aims to disseminate theoretical and practical knowledge about neural networks in measurement instrumentation and the related industrial applications It also creates a consciousness about the effectiveness of these techniques as well as the measurement problems in industrial environments

Security, Privacy, and Anonymity in Computation, Communication, and Storage Guojun Wang, Mohammed Atiquzzaman, Zheng Yan, Kim-Kwang Raymond Choo, 2017-12-11 This book constitutes the refereed proceedings of 11 symposia and workshops held at the 10th International Conference on Security Privacy and Anonymity in Computation Communication and Storage SpaCCS 2017 held in Guangzhou China in December 2017 The total of 75 papers presented in this volume was carefully reviewed and selected from a total of 190 submissions to all workshops UbiSafe 2017 The 9th IEEE International Symposium

on UbiSafe Computing ISSR 2017 The 9th IEEE International Workshop on Security in e Science and e Research TrustData 2017 The 8th International Workshop on Trust Security and Privacy for Big Data TSP 2017 The 7th International Symposium on Trust Security and Privacy for Emerging Applications SPIoT 2017 The 6th International Symposium on Security and Privacy on Internet of Things NOPE 2017 The 5th International Workshop on Network Optimization and Performance Evaluation DependSys 2017 The Third International Symposium on Dependability in Sensor Cloud and Big Data Systems and Applications SCS 2017 The Third International Symposium on Sensor Cloud Systems WCSSC 2017 The Second International Workshop on Cloud Storage Service and Computing MSCF 2017 The First International Symposium on Multimedia Security and Digital Forensics SPBD 2017 The 2017 International Symposium on Big Data and Machine Learning in Information Security Privacy and Anonymity

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