



Dry Processing For Submicrometer Lithography

Veronica Czitrom, Patrick D. Spagon



Dry Processing For Submicrometer Lithography:

Dry Processing for Submicrometer Lithography James Bondur, Alan R. Reinberg, 1990 **Dry Etching for VLSI** A.J. van Roosmalen, J.A.G. Baggerman, S.J.H. Brader, 2013-06-29 This book has been written as part of a series of scientific books being published by Plenum Press The scope of the series is to review a chosen topic in each volume To supplement this information the abstracts to the most important references cited in the text are reprinted thus allowing the reader to find in depth material without having to refer to many additional publications This volume is dedicated to the field of dry plasma etching as applied in silicon semiconductor processing Although a number of books have appeared dealing with this area of physics and chemistry these all deal with parts of the field This book is unique in that it gives a compact yet complete in depth overview of fundamentals systems processes tools and applications of etching with gas plasmas for VLSI Examples are given throughout the fundamental sections in order to give the reader a better insight in the meaning and magnitude of the many parameters relevant to dry etching Electrical engineering concepts are emphasized to explain the pros and cons of reactor concepts and excitation frequency ranges In the description of practical applications extensive use is made of cross referencing between processes and materials as well as theory and practice It is thus intended to provide a total model for understanding dry etching The book has been written such that no previous knowledge of the subject is required It is intended as a review of all aspects of dry etching for silicon semiconductor processing **Semiconductor Lithography** Wayne M. Moreau, 2012-12-06 Semiconductor lithography is one of the key steps in the manufacturing of integrated silicon based circuits In fabricating a semiconductor device such as a transistor a series of hot processes consisting of vacuum film deposition oxidations and dopant implantation are all patterned into microscopic circuits by the wet processes of lithography Lithography as adopted by the semiconductor industry is the process of drawing or printing the pattern of an integrated circuit in a resist material The pattern is formed and overlayed to a previous circuit layer as many as 30 times in the manufacture of logic and memory devices With the resist pattern acting as a mask a permanent device structure is formed by subtractive removal etching or by additive deposition of metals or insulators Each process step in lithography uses inorganic or organic materials to physically transform semiconductors of silicon insulators of oxides nitrides and organic polymers and metals into useful electronic devices All forms of electromagnetic radiation are used in the processing Lithography is a multidisciplinary science of materials processes and equipment interacting to produce three dimensional structures Many aspects of chemistry electrical engineering materials science and physics are involved The purpose of this book is to bring together the work of many scientists and engineers over the last 10 years and focus upon the basic resist materials the lithographic processes and the fundamental principles behind each lithographic process Proceedings of the ... Symposium on Plasma Processing ,1992 *Proceedings of the Tenth Symposium on Plasma Processing* Electrochemical Society. Dielectric Science and Technology Division, 1994 Handbook of VLSI Microlithography William B.

Glendinning, John N. Helbert, 2012-12-02 This handbook gives readers a close look at the entire technology of printing very high resolution and high density integrated circuit IC patterns into thin resist process transfer coatings including optical lithography electron beam ion beam and x ray lithography The book s main theme is the special printing process needed to achieve volume high density IC chip production especially in the Dynamic Random Access Memory DRAM industry The book leads off with a comparison of various lithography methods covering the three major patterning parameters of line space resolution line edge and pattern feature dimension control The book s explanation of resist and resist process equipment technology may well be the first practical description of the relationship between the resist process and equipment parameters The basics of resist technology are completely covered including an entire chapter on resist process defectivity and the potential yield limiting effect on device production Each alternative lithographic technique and testing method is considered and evaluated basic metrology including optical scanning electron microscope SEM techniques and electrical test devices along with explanations of actual printing tools and their design construction and performance The editor devotes an entire chapter to today s sophisticated complex electron beam printers and to the emerging x ray printing technology now used in high density CMOS devices Energetic ion particle printing is a controllable steerable technology that does not rely on resist and occupies a final section of the handbook Guidebook for Managing Silicon Chip Reliability Michael Pecht, Riko Radojcic, Gopal Rao, 2017-11-22 Examines the principal failure mechanisms associated with modern integrated circuits and describes common practices used to resolve them Provides a framework for how to model the mechanism test for defects and avoid and manage damage Focuses on device failure and causes Outlines how to establish the specifications defining chip performance cost quality and reliability Includes an extensive table detailing the types of mechanism failures *Applied Digital Optics* Bernard C. Kress, Patrick Meyrueis, 2009-11-04 Miniaturization and mass replications have begun to lead the optical industry in the transition from traditional analog to novel digital optics As digital optics enter the realm of mainstream technology through the worldwide sale of consumer electronic devices this timely book aims to present the topic of digital optics in a unified way Ranging from micro optics to nanophotonics and design to fabrication through to integration in final products it reviews the various physical implementations of digital optics in either micro refractives waveguide planar lightwave chips diffractive and hybrid optics or sub wavelength structures resonant gratings surface plasmons photonic crystals and metamaterials Finally it presents a comprehensive list of industrial and commercial applications that are taking advantage of the unique properties of digital optics Applied Digital Optics is aimed primarily at optical engineers and product development and technical marketing managers it is also of interest to graduate level photonics students and micro optic foundries Helps optical engineers review and choose the appropriate software tools to design model and generate fabrication files Gives product managers access to an exhaustive list of applications available in today s market for integrating such digital optics as well as where the next potential application of digital optics might be Provides a broad view for technical

marketing managers in all aspects of digital optics and how such optics can be classified Explains the numerical implementation of optical design and modelling techniques Enables micro optics foundries to integrate the latest fabrication and replication techniques and accordingly fine tune their own fabrication processes Statistical Case Studies for Industrial Process Improvement Veronica Czitrom, Patrick D. Spagon, 1997-01-01 This book contains a broad selection of case studies written by professionals in the semiconductor industry that illustrate the use of statistical methods to improve manufacturing processes These case studies offer engineers scientists technicians and managers numerous examples of best in class practices by their peers Because of the universal nature of statistical applications the methods described here can be applied to a wide range of industries including the chemical biotechnology automotive steel plastics textile and food industries Many industries already benefit from the use of statistical methods although the semiconductor industry is considered both a leader in and a model for the wide application and effective use of statistics **Advances in Resist Technology and Processing**, 1993 **Plasma Charging Damage** Kin P. Cheung, 2012-12-06 In the 50 years since the invention of transistor silicon integrated circuit IC technology has made astonishing advances A key factor that makes these advances possible is the ability to have precise control on material properties and physical dimensions The introduction of plasma processing in pattern transfer and in thin film deposition is a critical enabling advance among other things In state of the art silicon IC manufacturing process plasma is used in more than 20 different critical steps Plasma is sometimes called the fourth state of matter other than gas liquid and solid It is a mixture of ions positive and negative electrons and neutrals in a quasi neutral gaseous steady state very far from equilibrium sustained by an energy source that balances the loss of charged particles It is a very harsh environment for the delicate ICs Highly energetic particles such as ions electrons and photons bombard the surface of the wafer continuously These bombardments can cause all kinds of damage to the silicon devices that make up the integrated circuits **Manufacturing Techniques for Microfabrication and Nanotechnology** Marc J. Madou, 2011-06-13 Designed for science and engineering students this text focuses on emerging trends in processes for fabricating MEMS and NEMS devices The book reviews different forms of lithography subtractive material removal processes and additive technologies Both top down and bottom up fabrication processes are exhaustively covered and the merits of the different approaches are compared Students can use this color volume as a guide to help establish the appropriate fabrication technique for any type of micro or nano machine **Handbook of Polymer Science and Technology** Nicholas P. Cheremisinoff, 1989-08-11 Advanced Techniques for Integrated Circuit Processing Terry R. Turner, 1991 *Process Module Metrology, Control, and Clustering* Cecil J. Davis, 1992 □□□□□□□□□□□□□□□□ □□□□ □□ (Japan), 1972 □□□□□□□□□□□□□□□□ , 1989 **SPIE ... Publications Index**, 1992 **Handbook of Multilevel Metallization for Integrated Circuits** Syd R. Wilson, Clarence J. Tracy, John L. Freeman, 1993 It is widely recognized that the successful design development and integration of multilevel metallization MLM systems is and will continue to be key to

current and future VLSI technologies All major semiconductor companies have significant ongoing research teams focused in this area These teams must view multilevel metallization as a system rather than a collection of isolated process modules

Eighth International IEEE VLSI Multilevel Interconnection Conference, 1991 IEEE Electron Devices Society, 1991

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