

Conclusion

- **Concurrent algorithm design**
 - Theoretical design
 - Practical design (may be just as important)
 - Implementation
- **You need to know your hardware**
 - For correctness
 - For performance

Concurrent Hardware The Theory And Practice Of Self Timed Design

Karl M. Fant



Concurrent Hardware The Theory And Practice Of Self Timed Design:

Concurrent Hardware Mikhail Aleksandrovich Kišinevskij, 1994 Concurrent Hardware Michael Kishinevsky, Alex Kondratyev, Alexander Taubin, Victor Varshavsky, 1993-12-08 Examines the theory and design of self timed systems The logical design of self timed circuits STCs provides a focal point for on the one hand those interested in formal models of parallel computation and on the other hardware designers The approach taken by the authors is to address general issues concerning the very nature of concurrency as well as to demonstrate the particular features of asynchronous design The book presents formal models of the specification and verification of parallel processes and describes methods for self timed circuit synthesis and analysis It is augmented by a demonstration version of a CAD system called FORCAGE which consists of subsystems of behavior verification self timed circuit analysis and synthesis The system can be run on a PC **Concurrency and Hardware Design** Jordi Cortadella, Alex Yakovlev, Grzegorz Rozenberg, 2003-07-01 As CMOS semiconductor technology strides towards billions of transistors on a single die new problems arise on the way They are concerned with the minishing fabrication process features which affect for example the gate to wire delay ratio They manifest themselves in greater variations of size and operating parameters of devices which put the overall reliability of systems at risk And most of all they have tremendous impact on design productivity where the costs of utilizing the growing silicon real estate rocket to billions of dollars that have to be spent on design verification and testing All such problems call for new sign approaches and models for digital systems Furthermore new developments in non CMOS technologies such as single electron transistors rapid single quantum devices quantum dot cells molecular devices etc add extra demand for new research in system design methodologies What kind of models and design methodologies will be required to build systems in all these new technologies Answering this question even for each particular type of new technology generation is not easy especially because sometimes it is not even clear what kind of elementary devices are feasible there This problem is of an interdisciplinary nature It requires an bridges between different scientific communities The bridges must be built very quickly and be maximally exible to accommodate changes taking place in a logarithmic timescale Hardware Design and Petri Nets Alex Yakovlev, Luis Gomes, Luciano Lavagno, 2013-04-17 Hardware Design and Petri Nets presents a summary of the state of the art in the applications of Petri nets to designing digital systems and circuits The area of hardware design has traditionally been a fertile field for research in concurrency and Petri nets Many new ideas about modelling and analysis of concurrent systems and Petri nets in particular originated in theory of asynchronous digital circuits Similarly the theory and practice of digital circuit design have always recognized Petri nets as a powerful and easy to understand modelling tool The ever growing demand in the electronic industry for design automation to build various types of computer based systems creates many opportunities for Petri nets to establish their role of a formal backbone in future tools for constructing systems that are increasingly becoming distributed concurrent and asynchronous Petri nets have already proved very effective in supporting algorithms for

solving key problems in synthesis of hardware control circuits However since the front end to any realistic design flow in the future is likely to rely on more pragmatic Hardware Description Languages HDLs such as VHDL and Verilog it is crucial that Petri nets are well interfaced to such languages Hardware Design and Petri Nets is divided into five parts which cover aspects of behavioral modelling analysis and verification synthesis from Petri nets and STGs design environments based on high level Petri nets and HDLs and finally performance analysis using Petri nets Hardware Design and Petri Nets serves as an excellent reference source and may be used as a text for advanced courses on the subject

Algorithms for Synthesis and Testing of Asynchronous Circuits Luciano Lavagno,Alberto L. Sangiovanni-Vincentelli,2012-12-06 Since the second half of the 1980s asynchronous circuits have been the subject of a great deal of research following a period of relative oblivion The lack of interest in asynchronous techniques was motivated by the progressive shift towards synchronous design techniques that had much more structure and were much easier to verify and synthesize System design requirements made it impossible to eliminate totally the use of asynchronous circuits Given the objective difficulty encountered by designers the asynchronous components of electronic systems such as interfaces became a serious bottleneck in the design process The use of new models and some theoretical breakthroughs made it possible to develop asynchronous design techniques that were reliable and effective This book describes a variety of mathematical models and of algorithms that form the backbone and the body of a new design methodology for asyn chronous design The book is intended for asynchronous hardware designers for computer aided tool experts and for digital designers interested in ex ploring the possibility of designing asynchronous circuits It requires a solid mathematical background in discrete event systems and algorithms While the book has not been written as a textbook nevertheless it could be used as a reference book in an advanced course in logic synthesis or asynchronous design

Logically Determined Design Karl M. Fant,2005-02-22 This seminal book presents a new logically determined designmethodology for designing clockless circuit systems The bookpresents the foundations architectures and methodologies toimplement such systems Based on logical relationships itconcentrates on digital circuit system complexity and productivityto allow for more reliable faster and cheaper products Transcends shortcomings of Boolean logic Presents theoritical foundations architecture and analysis ofclockless asynchronous circuit design Contains examples and exercises making it ideal for thosestudying the area

Application and Theory of Petri Nets 2000 Mogens Nielsen,Dan Simpson,2003-06-26 This book constitutes the refereed proceedings of the 21st International Conference on Application and Theory of Petri Nets ICATPN 2000 held in Aarhus Denmark in June 2000 The 20 revised full papers presented together with four invited surveys and four tool presentations were carefully reviewed and selected from 57 submissions The papers address all current aspects of Petri net research and development including system design and verification UML compositionality process algebras model checking computer networking business process engineering communication networks etc Various classes of Petri nets are discussed including safe Petri nets high level Petri nets colored

Petri nets P T nets and timed Petri nets **Asynchronous Circuit Design** Chris J. Myers, 2004-04-05 With asynchronous circuit design becoming a powerful tool in the development of new digital systems circuit designers are expected to have asynchronous design skills and be able to leverage them to reduce power consumption and increase system speed This book walks readers through all of the different methodologies of asynchronous circuit design emphasizing practical techniques and real world applications instead of theoretical simulation The only guide of its kind it also features an ftp site complete with support materials Market Electrical Engineers Computer Scientists Device Designers and Developers in industry An Instructor Support FTP site is available from the Wiley editorial department **Self-Timed Control of Concurrent Processes** Victor I. Varshavsky, 2012-12-06 Et moi si j'avait su comment en revenir One service mathematics has rendered the human race It has put common sense back where it belongs on the topmost shelf next to Jules Verne to the dusty canister labelled discarded non The series is divergent therefore we may be sense Eric T Bell able to do something with it O Heavenside Mathematics is a tool for thought A highly necessary tool in a world where both feedback and non linearities abound Similarly all kinds of parts of mathematics serve as tools for other parts and for other sciences Applying a simple rewriting rule to the quote on the right above one finds such statements as One service topology has rendered mathematical physics One service logic has rendered computer science One service category theory has rendered mathematics All arguably true And all statements obtainable this way form part of the raison d'être of this series *Lectures on Petri Nets II: Applications* Wolfgang Reisig, Grzegorz Rozenberg, 1998-11-04 The two volume set originates from the Advanced Course on Petri Nets held in Dagstuhl Germany in September 1996 beyond the lectures given there additional chapters have been commissioned to give a well balanced presentation of the state of the art in the area Together with its companion volume *Lectures on Petri Nets I Basic Models* this book is the actual reference for the area and addresses professionals students lecturers and researchers who are interested in systems design and would like to learn to use Petri nets familiar with subareas of the theory or its applications and wish to view the whole area interested in learning about recent results presented within a unified framework planning to apply Petri nets in practical situations interested in the relationship of Petri nets to other models of concurrent systems *Algebraic Theory of Automata Networks* Pal Domosi, Christopher L. Nehaniv, 2005-01-01 Investigates automata networks as algebraic structures and develops their theory in line with other algebraic theories such as those of semigroups groups rings and fields The authors also investigate automata networks as products of automata that is as compositions of automata obtained by cascading without feedback or with feedback of various restricted types or most generally with the feedback dependencies controlled by an arbitrary directed graph They survey and extend the fundamental results in regard to automata networks including the main decomposition theorems of Letichevsky of Krohn and Rhodes and of others *Computer Aided Verification*, 2000 **Computer Aided Verification** Gerard Berry, Hubert Comon, Alain Finkel, 2003-05-15 This book constitutes the refereed proceedings of the 13th International

Conference on Computer Aided Verification CAV 2001 held in Paris France in July 2001 The 33 revised full papers presented were carefully reviewed and selected from 106 regular paper submissions also included are 13 reviewed tool presentations selected from 27 submissions The book offers topical sections on model checking and theorem proving automata techniques verification core technology BDD and decision trees abstraction and refinement combinations infinite state systems temporal logics and verification microprocessor verification and cache coherence SAT and applications and timed automata

Application and Theory of Petri Nets 1997 Pierre Azema, Gianfranco Balbo, 1997-06-04 This book constitutes the refereed proceedings of the 18th International Conference on the Application and Theory of Petri Nets ICATPN 97 held in Toulouse France in June 1997 The 22 revised full papers presented in the volume were selected from a total of 61 submissions also included are three invited contributions All relevant topics in the area are addressed Besides a variety of Petri net classes workflow management telecommunication networking constraint satisfaction program semantics concurrency and temporal logic are among the topics addressed Completion Detection in Asynchronous Circuits Pallavi Srivastava, 2022-11-08 This book is intended for designers with experience in traditional clocked circuit design seeking information about asynchronous circuit design in order to determine if it would be advantageous to adopt asynchronous methodologies in their next design project The author introduces a generic approach for implementing a deterministic completion detection scheme for asynchronous bundled data circuits that incorporates a data dependent computational process taking advantage of the average case delay The author validates the architecture using a barrel shifter as shifting is the basic operation required by all the processors The generic architecture proposed in this book for a deterministic completion detection scheme for bundled data circuits will facilitate researchers in considering the asynchronous design style for developing digital circuits **Carl Adam Petri: Ideas, Personality, Impact** Wolfgang Reisig, Grzegorz

Rozenberg, 2019-05-24 This book offers an overview of the key ideas of Petri nets how they were developed and how they were applied in diverse applications The chapters in the first part offer individual perspectives on the impact of Petri's work The second part of the book contains personal memories from researchers who collaborated with him closely in particular they recount his unique personality The chapters in the third part offer more conventional treatments on various aspects of current Petri net research and the fourth part examines the wide applications of Petri nets and the relationships with other domains The editors and authors are the leading researchers in this domain and this book will be a valuable insight for researchers in computer science particularly those engaged with concurrency and distributed systems **Lectures on**

Concurrency and Petri Nets Jörg Desel, Wolfgang Reisig, Grzegorz Rozenberg, 2004-06-14 This tutorial volume originates from the 4th Advanced Course on Petri Nets ACPN 2003 held in Eichstätt Germany in September 2003 In addition to lectures given at ACPN 2003 additional chapters have been commissioned to give a well balanced presentation of the state of the art in the area This book will be useful as both a reference for those working in the area as well as a study book for the reader

who is interested in an up to date overview of research and development in concurrent and distributed systems of course readers specifically interested in theoretical or applicational aspects of Petri nets will appreciate the book as well

2nd International Symposium on Advanced Research in Asynchronous Circuits and Systems Institute of Electrical and Electronics Engineers, Inc. Staff, 1996-03 Papers from the March 1996 symposium detail the latest knowledge in asynchronous hardware design in sections on high speed design logic synthesis architectural synthesis formal methods novel techniques design automation and measurements low power and system design and logic optimization The Sequential Optimization of Asynchronous and Synchronous Finite-State Machines Robert M. Fuhrer, Steven M. Nowick, 2012-12-06

Asynchronous or unclocked digital systems have several potential advantages over their synchronous counterparts In particular they address a number of challenging problems faced by the designers of large scale synchronous digital systems power consumption worst case timing constraints and engineering and design reuse issues associated with the use of a fixed rate global clock Moreover while for synchronous systems these problems are exacerbated by increasing system size asynchronous systems promise to scale more gracefully

Sequential Optimization of Asynchronous and Synchronous Finite State Machines Algorithms and Tools makes three contributions to the field of sequential optimization for finite state machines 1 it introduces several new provably optimal algorithms for the synthesis and optimization of asynchronous finite state machines FSMs 2 it presents practical software implementations of each of these algorithms and 3 it introduces a complete new CAD package called MINIMALIST binding these tools into a state of the art technology independent synthesis path for burst mode asynchronous circuits Throughout this book real world industrial designs are used as benchmark circuits to validate the usefulness of the tools As an additional benefit some of the theory and tools also provide new methods for the optimization of synchronous FSMs

Computer Architecture '97 Ronald Pose, 1997-04-01 The Australasian Computer Architecture Conferences were established in recognition of the fundamental reliance of all computer systems on the integrity capability and performance of the underlying computer architecture ACAC 97 held in February 1997 in Sydney Australia was the second in this series and attracted authors from Singapore Hong Kong Korea Sweden New Zealand Spain China the United States of America and Australia This volume contains a selection of 21 research papers the best and most interesting of those presented at the conference highlighting technologies to increase processor performance asynchronous processor designs interconnection networks and routing and parallel and distributed multiprocessor systems

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