### CECIL HASTINGS JR.

# Approximations for Digital Computers



## **Approximations For Digital Computers**

**Neal G. Anderson, Gualtiero Piccinini** 

#### **Approximations For Digital Computers:**

**Approximations for Digital Computers** Cecil Hastings Jr., Jeanne T. Wayward, James P. Wong Jr., 2015-12-08 Numerical analysts and computer operators in all fields will welcome this publication in book form of Cecil Hastings well known approximations for digital computers formerly issued in loose sheets and available only to a limited number of specialists In a new method that combines judgment and intuition with mathematics Mr Hasting has evolved a set of approximations which far surpasses in simplicity earlier approximations developed by conventional methods Part I of this book introduces the collection of useful and illustrative approximations each of which is presented with a carefully drawn error curve in Part II Originally published in 1955 The Princeton Legacy Library uses the latest print on demand technology to again make available previously out of print books from the distinguished backlist of Princeton University Press These editions preserve the original texts of these important books while presenting them in durable paperback and hardcover editions The goal of the Princeton Legacy Library is to vastly increase access to the rich scholarly heritage found in the thousands of books published by Princeton University Press since its founding in 1905 Approximations for Digital Computers Cecil Hastings, Jeanne T. Wayward, James P. Wong, 2016-04-19 This monograph deals with the subject of best approximation in the sense of Chebyshev as applied to the problem of making univariate functional data available to the high speed digital computing machine Our investigation is of a numerical and empirical nature Part I of this book serves as an introduction to the collection of approximations given in Part II Part II contains the Approximations for Digital Computers formerly issued as a cumulative publication of loose sheets and made available to numerical analysts upon request Each sheet of the seventy odd issued in this series contains an approximation of a useful or illustrative nature presented with a carefully drawn error Approximations for Digital Computers Cecil Hastings, James P. Wong, J.T. Hayward, 1955 *Approximations for* curve Digital Computers, By Cecil Hastings, Jr., Assisted by Jeanne T. Hayward (And) James P. Wong, Jr Cecil Hastings, 1955

Digital Computer User's Handbook Melvin Klerer,1967 Compendium of digital computer procedures covers systems design language functions simulation mathematical analysis and statistical methodologies information storage and retrieval supply systems linear programming etc and includes a glossary of sorting and merging terms a list of computer equipment together with characteristics thereof and a directory of firms of the electronics industry manufacturing computers Bibliographys and references after each chapter Numerical Recipes 3rd Edition William H. Press,2007-09-06 Do you want easy access to the latest methods in scientific computing This greatly expanded third edition of Numerical Recipes has it with wider coverage than ever before many new expanded and updated sections and two completely new chapters The executable C code now printed in colour for easy reading adopts an object oriented style particularly suited to scientific applications Co authored by four leading scientists from academia and industry Numerical Recipes starts with basic mathematics and computer science and proceeds to complete working routines The whole book is presented in the informal easy to read style

that made earlier editions so popular Highlights of the new material include a new chapter on classification and inference Gaussian mixture models HMMs hierarchical clustering and SVMs a new chapter on computational geometry covering KD trees quad and octrees Delaunay triangulation and algorithms for lines polygons triangles and spheres interior point methods for linear programming MCMC an expanded treatment of ODEs with completely new routines and many new statistical distributions For support or to subscribe to an online version please visit www nr com *Digital Computers in Research*Bert F. Green,1963 Textbook on computer programming and an account of the applications of computers to behavioural and social research problems includes digital codes information processing and theoretic foundations Bibliography pp 307 to 316

Digital Computer Arithmetic Datapath Design Using Verilog HDL James E. Stine, 2012-12-06 The role of arithmetic in datapath design in VLSI design has been increasing in importance over the last several years due to the demand for processors that are smaller faster and dissipate less power Unfortunately this means that many of these datapaths will be complex both algorithmically and circuit wise As the complexity of the chips increases less importance will be placed on understanding how a particular arithmetic datapath design is implemented and more importance will be given to when a product will be placed on the market This is because many tools that are available today are automated to help the digital system designer maximize their efficiently Unfortunately this may lead to problems when implementing particular datapaths The design of high performance architectures is becoming more complicated because the level of integration that is capable for many of these chips is in the billions Many engineers rely heavily on software tools to optimize their work therefore as designs are getting more complex less understanding is going into a particular implementation because it can be generated automati cally Although software tools are a highly valuable asset to designer the value of these tools does not diminish the importance of understanding datapath elements Therefore a digital system designer should be aware of how algorithms can be implemented for datapath elements Unfortunately due to the complex ity of some of these algorithms it is sometimes difficult to understand how a particular algorithm is implemented without seeing the actual code High Speed Digital Filtering by Convolution Approximation E. B. Weis, 1967 This report discusses a technique for digital filtering by convolution approximation which is an acceptable compromise between accuracy and speed This technique is applicable where high accuracy is not necessary and where a digital computer with elaborate processing capability is not available Most requirements for digital filtering can be developed in terms of the approximations suggested in this report By casting the required filter in a form that is most amenable to numerical computation the accuracy of approximation is maximized If power of two accuracy is insufficient there is a continuous tradeoff between accuracy and speed which involves range division and more bits in the approximation Programming and Utilizing Digital Computers Robert Steven Ledley, 1962

RAND and the Information Evolution Willis H. Ware, Peter Chalk, Richard Warnes, Lindsay Clutterbuck, Aidan Kirby Winn, Sheila Nataraj Kirby, 2008-12-09 This professional memoir describes RANDs contributions to the evolution of computer

science particularly during the first decades following World War II when digital computers succeeded slide rules mechanical desk calculators electric accounting machines and analog computers The memoir includes photographs and vignettes that reveal the collegial creative and often playful spirit in which the groundbreaking research was conducted at RAND First Course in Numerical Analysis Anthony Ralston, Philip Rabinowitz, 2001-01-01 Outstanding text oriented toward computer solutions stresses errors in methods and computational efficiency Problems some strictly mathematical others requiring a computer appear at the end of each chapter Scientific and Technical Aerospace Reports, 1979 **Origins of Digital Computers** B. Randell,2013-12-21 Digital Computer Newsletter .1949 The Physical Signature of Computation Neal G. Anderson, Gualtiero Piccinini, 2024-06-24 In The Physical Signature of Computation Neal Anderson and Gualtiero Piccinini articulate and defend the robust mapping account the most systematic rigorous and comprehensive account of computational implementation to date Drawing in part from recent results in physical information theory they argue that mapping accounts of implementation can be made adequate by incorporating appropriate physical constraints According to the robust mapping account the key constraint on mappings from physical to computational states the key for establishing that a computation is physically implemented is physical computational equivalence evolving physical states bear neither more nor less information about the evolving computation than do the computational states they map onto When this highly nontrivial constraint is satisfied among others that are spelled out as part of the account a physical system can be said to implement a computation in a robust sense which means that the system bears the physical signature of the computation Anderson and Piccinini apply their robust mapping account to important questions in physical foundations of computation and cognitive science including the alleged indeterminacy of computation pancomputationalism and the computational theory of mind They show that physical computation is determinate nontrivial versions of pancomputationalism fail and cognition involves computation only insofar as neurocognitive systems bear the physical signature of specific computations They also argue that both consciousness and physics outstrip computation Digital Computer Design Edward L. Braun, 2014-05-12 Digital Computer Design Logic Circuitry and Synthesis focuses on the logical structure electronic realization and application of digital information processors. The manuscript first offers information on numerical symbols fundamentals of computing aids quantization representation of numbers in an electronic digital computer and computer applications The text then ponders on the nature of automatic computation and Boolean algebra Discussions focus on the advantages of a Boolean algebraic description of a digital computer clock pulse generators and timing circuits sequential switching networks elements of information processing systems and types of digital computers and automatic sequencing methods The book elaborates on circuit descriptions of switching and storage elements and large capacity storage systems Topics include static magnetic storage dynamic delay line storage cathode ray storage vacuum tube systems of circuit logic and magnetic core systems of circuit logic The publication also examines the system design of GP

computers digital differential analyzer and the detection and correction of errors The text is a valuable source of data for mathematicians and engineers interested in digital computer design — Computers and Data Processing Systems ,1962 — Computer Literature Bibliography United States. National Bureau of Standards,1965 — NBS Special Publication ,1968

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