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Stochastic Approaches to Electron Transport in Micro- and Nanostructures Mihail Nedjalkov, Ivan Dimov, Siegfried Selberherr, 2021-04-05 The book serves as a synergistic link between the development of mathematical models and the emergence of stochastic Monte Carlo methods applied for the simulation of current transport in electronic devices Regarding the models the historical evolution path beginning from the classical charge carrier transport models for microelectronics to current quantum based nanoelectronics is explicatively followed Accordingly the solution methods are elucidated from the early phenomenological single particle algorithms applicable for stationary homogeneous physical conditions up to the complex algorithms required for quantum transport based on particle generation and annihilation The book fills the gap between monographs focusing on the development of the theory and the physical aspects of models their application and their solution methods and monographs dealing with the purely theoretical approaches for finding stochastic solutions of Fredholm integral equations

Microwave and RF Semiconductor Control Device Modeling Robert H. Caverly, 2016-02-01 This comprehensive new resource presents a detailed look at the modeling and simulation of microwave semiconductor control devices and circuits Fundamental PIN MOSFET and MESFET nonlinear device modeling are discussed including the analysis of transient and harmonic behavior Considering various control circuit topologies the book analyzes a wide range of models from simple approximations to sophisticated analytical approaches Readers find clear examples that provide guidance in how to use specific modeling techniques for their challenging projects in the field Numerous illustrations help practitioners better understand important device and circuit behavior revealing the relationship between key parameters and results This authoritative volume covers basic and complex mathematical models for the most common semiconductor control elements used in today s microwave and RF circuits and systems

Modeling Nanowire and Double-Gate Junctionless Field-Effect Transistors Farzan Jazaeri, Jean-Michel Sallese, 2018-03 A detailed introduction to the design modeling and operation of junctionless field effect transistors FETs including advantages and limitations

From Contamination to Defects, Faults and Yield Loss Jitendra B. Khare, Wojciech Maly, 2012-12-06 Over the years there has been a large increase in the functionality available on a single integrated circuit This has been mainly achieved by a continuous drive towards smaller feature sizes larger dies and better packing efficiency However this greater functionality has also resulted in substantial increases in the capital investment needed to build fabrication facilities Given such a high level of investment it is critical for IC manufacturers to reduce manufacturing costs and get a better return on their investment The most obvious method of reducing the manufacturing cost per die is to improve manufacturing yield Modern VLSI research and engineering which includes design manufacturing and testing encompasses a very broad range of disciplines such as chemistry physics material science circuit design mathematics and computer science Due to this diversity the VLSI arena has become fractured into a number of separate sub domains with little or no interaction between them This is the case with the

relationships between testing and manufacturing From Contamination to Defects Faults and Yield Loss Simulation and Applications focuses on the core of the interface between manufacturing and testing i e the contamination defect fault relationship The understanding of this relationship can lead to better solutions of many manufacturing and testing problems Failure mechanism models are developed and presented which can be used to accurately estimate probability of different failures for a given IC This information is critical in solving key yield related applications such as failure analysis fault modeling and design manufacturing

Practical Handbook of Photovoltaics Augustin McEvoy, Tom Markvart, Luis Castaner, T. Markvart, L. Castaner, 2003-10-30 As part of the growing sustainable and renewable energy movement the design manufacture and use of photovoltaic devices is increasing in pace and frequency The Handbook of Photovoltaics will be a benchmark publication for those involved in the design manufacture and use of these devices The Handbook covers the principles of solar cell function the raw materials photovoltaic systems standards calibration testing economics and case studies The editors have assembled a cast of internationally respected contributors from industry and academia The report is essential reading for Physicists electronic engineers designers of systems installers architects policy makers relating to photovoltaics

Solar Cells Tom Markvart, Luis Castaner, 2004-12-15 The capture and use of solar energy has been growing for many years but only in recent times have advances in design and manufacture allowed us to see the incorporation of solar energy as a significant player in the renewable energy arena Solar cells are at the heart of any photovoltaic system and in this book the various types are described and their characteristics reviewed Going beyond materials design and function Solar Cells also covers their testing monitoring and calibration thus providing a comprehensive account of current activity in this important field of research and industry Solar Cells has been abstracted from the recent Practical Handbook of Photovoltaics by the same editors ISBN 185617 3909 2003 Elsevier Internationally respected contributors from industry and academia Abstracted from The Practical Handbook of Photovoltaics by the same Editors A comprehensive source book on all aspects of solar cells

Physics of Failure in Electronics M. E. Goldberg, M. F. Goldberg, Joseph Vaccaro, 1963

[Lifetime Factors in Silicon](#), 1980

Insulated Gate Bipolar Transistor IGBT Theory and Design Vinod Kumar Khanna, 2004-03-22 A comprehensive and state of the art coverage of the design and fabrication of IGBT All in one resource Explains the fundamentals of MOS and bipolar physics Covers IGBT operation device and process design power modules and new IGBT structures

NASA Conference Publication, 1977

Tradeoffs and Optimization in Analog CMOS Design David Binkley, 2008-09-15 Analog CMOS integrated circuits are in widespread use for communications entertainment multimedia biomedical and many other applications that interface with the physical world Although analog CMOS design is greatly complicated by the design choices of drain current channel width and channel length present for every MOS device in a circuit these design choices afford significant opportunities for optimizing circuit performance This book addresses tradeoffs and optimization of device and circuit performance for selections of the drain

current inversion coefficient and channel length where channel width is implicitly considered The inversion coefficient is used as a technology independent measure of MOS inversion that permits design freely in weak moderate and strong inversion This book details the significant performance tradeoffs available in analog CMOS design and guides the designer towards optimum design by describing An interpretation of MOS modeling for the analog designer motivated by the EKV MOS model using tabulated hand expressions and figures that give performance and tradeoffs for the design choices of drain current inversion coefficient and channel length performance includes effective gate source bias and drain source saturation voltages transconductance efficiency transconductance distortion normalized drain source conductance capacitances gain and bandwidth measures thermal and flicker noise mismatch and gate and drain leakage current Measured data that validates the inclusion of important small geometry effects like velocity saturation vertical field mobility reduction drain induced barrier lowering and inversion level increases in gate referred flicker noise voltage In depth treatment of moderate inversion which offers low bias compliance voltages high transconductance efficiency and good immunity to velocity saturation effects for circuits designed in modern low voltage processes Fabricated design examples that include operational transconductance amplifiers optimized for various tradeoffs in DC and AC performance and micropower low noise preamplifiers optimized for minimum thermal and flicker noise A design spreadsheet available at the book web site that facilitates rapid optimum design of MOS devices and circuits Tradeoffs and Optimization in Analog CMOS Design is the first book dedicated to this important topic It will help practicing analog circuit designers and advanced students of electrical engineering build design intuition rapidly optimize circuit performance during initial design and minimize trial and error circuit simulations

Operation and Modeling of the MOS Transistor Yannis Tsividis, Colin McAndrew, 2011 The MOS

Metal Oxide Semiconductor transistor is the most important building block of modern silicon integrated circuits This book fills an important gap in the literature by presenting a unified treatment of the operation and modeling of the MOS transistor that is complemented with extensive intuitive discussions The MOS transistor is the dominant VLSI Very Large Scale Integration device and understanding of this device is mandatory for those people planning a career in device physics and modeling as well as in circuit design Especially important for university courses there is a logical systematic and progressive description that starts with semiconductor fundamentals and builds up to a comprehensive understanding of the basics of MOS transistors For practicing professionals there are details of nuances observed in MOS transistor behavior and various approaches to modeling these are presented Detailed derivations are given for modeling dc currents charges for large signal operation small signal operation at low frequencies and high frequencies and noise

Official Gazette of the United States

Patent and Trademark Office United States. Patent and Trademark Office, 1992-06

Cryogenic Microelectronic Systems

for Ultra-Low Energy and Enhanced Performance Nurzhan Zhuldassov, Eby G. Friedman, 2025-09-18 This book explores cryogenic computers to achieve faster operation and lower energy use As computer components become smaller and

generate more heat traditional cooling methods struggle to keep up By operating at cryogenic temperatures these limitations can be overcome reducing heat improving performance and opening new possibilities for important applications such as large scale data centers and quantum computers The approaches and physical models discussed in this book are valuable since these concepts offer a practical methodology for increasing computational computing power without being limited by heat and power dissipation The book explores how cryogenic temperatures can supercharge computing Novel methods for designing and optimizing computer systems that operate at extremely low temperatures improve performance reduce power consumption and tackle the fundamental physical limits faced by modern electronics are introduced in this book From foundational physics based principles and cryogenic equipment to innovative graph theoretic design the book offers a fresh look at the future of high performance energy efficient computing

The Wigner Monte Carlo Method for Nanoelectronic Devices Damien Querlioz,Philippe Dollfus,2013-03-01 The emergence of nanoelectronics has led us to renew the concepts of transport theory used in semiconductor device physics and the engineering community It has become crucial to question the traditional semi classical view of charge carrier transport and to adequately take into account the wave like nature of electrons by considering not only their coherent evolution but also the out of equilibrium states and the scattering effects This book gives an overview of the quantum transport approaches for nanodevices and focuses on the Wigner formalism It details the implementation of a particle based Monte Carlo solution of the Wigner transport equation and how the technique is applied to typical devices exhibiting quantum phenomena such as the resonant tunnelling diode the ultra short silicon MOSFET and the carbon nanotube transistor In the final part decoherence theory is used to explain the emergence of the semi classical transport in nanodevices

Energy: a Continuing Bibliography with Indexes,1981
Energy, **Simulation of Semiconductor Devices and Processes, Vol. 5** Siegfried Selberherr,Hannes Stippel,Ernst Strasser,1993

MOSFET Technologies for Double-Pole Four-Throw Radio-Frequency Switch Viranjay M. Srivastava,Ghanshyam Singh,2013-10-07 This book provides analysis and discusses the design of various MOSFET technologies which are used for the design of Double Pole Four Throw DP4T RF switches for next generation communication systems The authors discuss the design of the DP4T RF switch by using the Double Gate DG MOSFET as well as the Cylindrical Surrounding double gate CSDG MOSFET The effect of HfO₂ high dielectric material in the design of DG MOSFET and CSDG MOSFET is also explored Coverage includes comparison of Single gate MOSFET and Double gate MOSFET switching parameters as well as testing of MOSFETs parameters using image acquisition

Defect and Fault Tolerance in VLSI Systems Israel Koren,2012-12-06 This book contains an edited selection of papers presented at the International Workshop on Defect and Fault Tolerance in VLSI Systems held October 6 7 1988 in Springfield Massachusetts Our thanks go to all the contributors and especially the members of the program committee for the difficult and time consuming work involved in selecting the papers that were presented in the workshop and reviewing the papers included in

this book Thanks are also due to the IEEE Computer Society in particular the Technical Committee on Fault Tolerant Computing and the Technical Committee on VLSI and the University of Massachusetts at Amherst for sponsoring the workshop and to the National Science Foundation for supporting under grant number MIP 8803418 the keynote address and the distribution of this book to all workshop attendees The objective of the workshop was to bring together researchers and practitioners from both industry and academia in the field of defect tolerance and yield enhancement in VLSI to discuss their mutual interests in defect tolerant architectures and models for integrated circuit defects faults and yield Progress in this area was slowed down by the proprietary nature of yield related data and by the lack of appropriate forums for disseminating such information The goal of this workshop was therefore to provide a forum for a dialogue and exchange of views A follow up workshop in October 1989 with C H Stapper from IBM and V K Jain from the University of South Florida as general co chairmen is being organized

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