Circuit Analysis with Computer Methods

Wing, Omar

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Circuit Theory And Computer Methods

Jiri Vlach, Kishore Singhal

Circuit Theory And Computer Methods:

Circuit Theory with Computer Methods Omar Wing, 1978 Applied Circuit Theory P. R. Adby 1980 Computer Methods for Circuit Analysis and Design Jirí Vlach, Kishore Singhal, 1983-08-31 **Computer Methods for Circuit** Analysis and Design Jiri Vlach, Kishore Singhal, 1994 This text is about methods used for the computer simulation of analog systems It concentrates on electronic applications but many of the methods are applicable to other engineering problems as well This revised edition 1st 1983 encompasses recent theoretical developments and program writing tips for computer aided design About 60% of the text is suitable for a senior level course in circuit theory. The whole text is suitable for graduate courses or as a reference for scientists and engineers who seek information in the field Annotation copyright by Book News Circuit Theory -with Computer Methods- O. Wing, Solutions Manual for Circuit Theory with Inc Portland OR Computer Methods Omar Wing, 1972 Computer Methods for Analysis of Mixed-Mode Switching Circuits Fei Yuan, Ajoy Opal, 2007-05-08 Computer Methods for Analysis of Mixed Mode Switching Circuits provides an in depth treatment of the principles and implementation details of computer methods and numerical algorithms for analysis of mixed mode switching circuits Major topics include Computer oriented formulation of mixed mode switching circuits Network functions of linear and nonlinear time varying systems Numerical Laplace inversion based integration algorithms and inconsistent initial conditions Time domain analysis of periodically switched linear and nonlinear circuits including response sensitivity noise clock jitter and statistical quantities Time domain analysis of circuits with internally controlled switches and over sampled sigma delta modulators Tellegen s theorem frequency reversal theorem and transfer function theorem of periodically switched linear circuits and their applications Frequency domain analysis of periodically switched linear and nonlinear circuits including response sensitivity group delay noise and statistical quantities Computer methods of circuit analysis Omar Wing, 1975 Interval Methods for Circuit Analysis L. V. Kolev, 1993 Written by an electrical engineer this book presents a novel approach in electric circuit theory which is based on interval analysis an intensively developing branch or applied mathematics Covering major topics in both circuit and system theory and their applications it suggests a variety of methods that are suited for handling linear and nonlinear analysis problems in which some or all of the relevant data are given as intervals Detailed algorithms of the interval methods presented are developed enabling their easy implementation on computers For the convenience of the reader a comprehensive survey of all the necessary interval analysis notions and techniques is provided in the introductory text Most of the theoretical developments considered in the book are also clearly illustrated through numerical examples Linear Circuit Theory Jiri Vlach, 2016-04-19 This comprehensive textbook covers all subjects on linear circuit theory with the emphasis on learning the subject without an excessive amount of information This unique approach stresses knowledge rather than computer use to start and differs from other books by introducing matrix algebra early in the book The book s 290 problems are meant to b Fundamentals of Electric Circuit Theory D

Chattopadhyay | PC Rakshit, 2000-11 This book presents the subject matter in a clear and concise manner with numerous Computer-Aided Analysis of Active Circuits Adrian Ioinovici, 1990-07-27 diagrams and examples Nonsmooth Modeling and Simulation for Switched Circuits Vincent Acary, Olivier Bonnefon, Bernard Brogliato, 2010-10-19 Nonsmooth Modeling and Simulation for Switched Circuits concerns the modeling and the numerical simulation of switched circuits with the nonsmooth dynamical systems NSDS approach using piecewise linear and multivalued models of electronic devices like diodes transistors switches Numerous examples ranging from introductory academic circuits to various types of power converters are analyzed and many simulation results obtained with the INRIA open source SICONOS software package are presented Comparisons with SPICE and hybrid methods demonstrate the power of the NSDS approach Nonsmooth Modeling and Simulation for Switched Circuits is intended to researchers and engineers in the field of circuits simulation and design but may also attract applied mathematicians interested by the numerical analysis for nonsmooth dynamical systems as well as researchers from Systems and Control Classical Circuit Theory Omar Wing, 2008-09-18 Classical circuit theory is a mathematical theory of linear passive circuits namely circuits composed of resistors capacitors and inductors Like many a thing classical it is old and enduring structured and precise simple and elegant It is simple in that everything in it can be deduced from rst principles based on a few physical laws It is enduring in that the things we can say about linear passive circuits are universally true unchanging No matter how complex a circuit may be as long as it consists of these three kinds of elements its behavior must be as prescribed by the theory The theory tells us what circuits can and cannot do As expected of any good theory classical circuit theory is also useful Its ulti mate application is circuit design The theory leads us to a design methodology that is systematic and precise It is based on just two fundamental theorems that the impedance function of a linear passive circuit is a positive real function and that the transfer function is a bounded real function of a complex variable University of Michigan Official Publication ,1965 **General Register** University of Michigan, 1964 Announcements for the following year included in some vols

Announcement University of Michigan. College of Engineering, 1962 Catalogue of the University of Michigan University of Michigan, 1963 Announcements for the following year included in some vols Scientific and Technical Aerospace Reports, 1970 **Symbolic Analysis and Reduction of VLSI Circuits** Zhanhai Qin, Chung-Kuan Cheng, 2009-03-13 Symbolic analysis is an intriguing topic in VLSI designs The analysis methods are crucial for the applications to the parasitic reduction and analog circuit evaluation However analyzing circuits symbolically remains a challenging research issue Therefore in this book we survey the recent results as the progress of on going works rather than as the solution of the field For parasitic reduction we approximate a huge amount of electrical parameters into a simplified RLC network This reduction allows us to handle very large integrated circuits with given memory capacity and CPU time A symbolic analysis approach reduces the circuit according to the network topology Thus the designer can maintain the meaning of the original network and perform the analysis hierarchically For

analog circuit designs symbolic analysis provides the relation between the tunable parameters and the characteristics of the circuit The analysis allows us to optimize the circuit behavior The book is divided into three parts Part I touches on the basics of circuit analysis in time domain and in s domain For an s domain expression the Taylor s expansion with s approaching infinity is equivalent to the time domain solution after the inverse Laplace transform On the other hand the Taylor s expansion when s approaches zero derives the moments of the output responses in time domain Part II focuses on the techniques for parasitic reduction In Chapter 2 we present the approximation methods to match the first few moments with reduced circuit orders In Chapter 3 we apply the Y Delta transformation to reduce the dynamic linear network The method finds the exact values of the low order coefficients of the numerator and denominator of the transfer function and thus matches part of the moments In Chapter 4 we handle two major issues of the Y Delta transformation common factors in fractional expressions and round off errors Chapter 5 explains the stability of the reduced expression in particular the Ruth Hurwitz Criterion We make an effort to describe the proof of the Criterion because the details are omitted in most of the contemporary textbooks In Chapter 6 we present techniques to synthesize circuits to approximate the reduced expressions after the transformation In Part III we discuss symbolic generation of the determinants and cofactors for the application to analog designs In Chapter 7 we depict the classical topological analysis approach In Chapter 8 we describe a determinant decision diagram approach that exploits the sparsity of the matrix to accelerate the computation In Chapter 9 we take only significant terms when we search through determinant decision diagram to approximate the solution In Chapter 10 we extend the determinant decision diagram to a hierarchical model The construction of the modules through the hierarchy is similar to the Y Delta transformation in the sense that a byproduct of common factors appears in the numerator and denominator Therefore we describe the method to prune the common factors

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