

A photograph of the Golden Gate Bridge in San Francisco at dusk. The bridge's orange-red towers and suspension cables are visible against a dark, cloudy sky. The water below is a deep blue, and the city lights are visible in the distance. The text is overlaid on the right side of the image.

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Ieee International Solid State Circuits Conference Paperback

Yuan Xie



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1975 IEEE International Solid-State Circuits Conference, 1975 Variation-Aware Adaptive Voltage Scaling for Digital CMOS Circuits Martin Wirnshofer, 2013-02-15 Increasing performance demands in integrated circuits together with limited energy budgets force IC designers to find new ways of saving power One innovative way is the presented adaptive voltage scaling scheme which tunes the supply voltage according to the present process voltage and temperature variations as well as aging The voltage is adapted on the fly by means of in situ delay monitors to exploit unused timing margin produced by state of the art worst case designs This book discusses the design of the enhanced in situ delay monitors and the implementation of the complete control loop comprising the monitors a control logic and an on chip voltage regulator An analytical Markov based model of the control loop is derived to analyze its robustness and stability Variation Aware Adaptive Voltage Scaling for Digital CMOS Circuits provides an in depth assessment of the proposed voltage scaling scheme when applied to an arithmetic and an image processing circuit This book is written for engineers interested in adaptive techniques for low power CMOS circuits **Analog Circuit Design for Process Variation-Resilient Systems-on-a-Chip** Marvin Onabajo, Jose Silva-Martinez, 2012-03-08 This book describes several techniques to address variation related design challenges for analog blocks in mixed signal systems on chip The methods presented are results from recent research works involving receiver front end circuits baseband filter linearization and data conversion These circuit level techniques are described with their relationships to emerging system level calibration approaches to tune the performances of analog circuits with digital assistance or control Coverage also includes a strategy to utilize on chip temperature sensors to measure the signal power and linearity characteristics of analog RF circuits as demonstrated by test chip measurements Describes a variety of variation tolerant analog circuit design examples including from RF front ends high performance ADCs and baseband filters Includes built in testing techniques linked to current industrial trends Balances digitally assisted performance tuning with analog performance tuning and mismatch reduction approaches Describes theoretical concepts as well as experimental results for test chips designed with variation aware techniques **Ultra-Low-Power Short-Range Radios** Patrick P. Mercier, Anantha P. Chandrakasan, 2015-07-21 This book explores the design of ultra low power radio frequency integrated circuits RFICs with communication distances ranging from a few centimeters to a few meters The authors describe leading edge techniques to achieve ultra low power communication over short range links Many different applications are covered ranging from body area networks to transcutaneous implant communications and smart appliance sensor networks Various design techniques are explained to facilitate each of these applications **Enabling Technologies for Very Large-Scale Synaptic Electronics** Themis Prodromakis, Alexantrou Serb, 2018-07-05 An important part of the colossal effort associated with the understanding of the brain involves using electronics hardware technology in order to reproduce biological behavior in silico The idea revolves around leveraging decades of experience in the electronics industry

as well as new biological findings that are employed towards reproducing key behaviors of fundamental elements of the brain notably neurons and synapses at far greater speed scale products than any software only implementation can achieve for the given level of modelling detail So far the field of neuromorphic engineering has proven itself as a major source of innovation towards the silicon brain goal with the methods employed by its community largely focused on circuit design analogue digital and mixed signal and standard commercial Complementary Metal Oxide Silicon CMOS technology as the preferred tools of choice when trying to simulate or emulate biological behavior However alongside the circuit oriented sector of the community there exists another community developing new electronic technologies with the express aim of creating advanced devices beyond the capabilities of CMOS that can intrinsically simulate neuron or synapse like behavior A notable example concerns nanoelectronic devices responding to well defined input signals by suitably changing their internal state weight thereby exhibiting synapse like plasticity This is in stark contrast to circuit oriented approaches where the synaptic weight variable has to be first stored typically as charge on a capacitor or digitally and then appropriately changed via complicated circuitry The shift of very much complexity from circuitry to devices could potentially be a major enabling factor for very large scale synaptic electronics particularly if the new devices can be operated at much lower power budgets than their corresponding traditional circuit replacements To bring this promise to fruition synergy between the well established practices of the circuit oriented approach and the vastness of possibilities opened by the advent of novel nanoelectronic devices with rich internal dynamics is absolutely essential and will create the opportunity for radical innovation in both fields The result of such synergy can be of potentially staggering impact to the progress of our efforts to both simulate the brain and ultimately understand it In this Research Topic we wish to provide an overview of what constitutes state of the art in terms of enabling technologies for very large scale synaptic electronics with particular stress on innovative nanoelectronic devices and circuit system design techniques that can facilitate the development of very large scale brain inspired electronic systems

CMOS Sigma-Delta Converters Jose M. de la Rosa, Rocio del Rio, 2013-03-13 A comprehensive overview of Sigma Delta Analog to Digital Converters ADCs and a practical guide to their design in nano scale CMOS for optimal performance This book presents a systematic and comprehensive compilation of sigma delta converter operating principles the new advances in architectures and circuits design methodologies and practical considerations going from system level specifications to silicon integration packaging and measurements with emphasis on nanometer CMOS implementation The book emphasizes practical design issues from high level behavioural modelling in MATLAB SIMULINK to circuit level implementation in Cadence Design FrameWork II As well as being a comprehensive reference to the theory the book is also unique in that it gives special importance on practical issues giving a detailed description of the different steps that constitute the whole design flow of sigma delta ADCs The book begins with an introductory survey of sigma delta modulators their fundamentals architectures and synthesis methods covered in Chapter 1 In Chapter 2 the effect of main circuit error

mechanisms is analysed providing the necessary understanding of the main practical issues affecting the performance of sigma delta modulators The knowledge derived from the first two chapters is presented in the book as an essential part of the systematic top down bottom up synthesis methodology of sigma delta modulators described in Chapter 3 where a time domain behavioural simulator named SIMSIDES is described and applied to the high level design and verification of sigma delta ADCs Chapter 4 moves farther down from system level to the circuit and physical level providing a number of design recommendations and practical recipes to complete the design flow of sigma delta modulators To conclude the book Chapter 5 gives an overview of the state of the art sigma delta ADCs which are exhaustively analysed in order to extract practical design guidelines and to identify the incoming trends design challenges as well as practical solutions proposed by cutting edge designs Offers a complete survey of sigma delta modulator architectures from fundamentals to state of the art topologies considering both switched capacitor and continuous time circuit implementations Gives a systematic analysis and practical design guide of sigma delta modulators from a top down bottom up perspective including mathematical models and analytical procedures behavioural modeling in MATLAB SIMULINK macromodeling and circuit level implementation in Cadence Design FrameWork II chip prototyping and experimental characterization Systematic compilation of cutting edge sigma delta modulators Complete description of SIMSIDES a time domain behavioural simulator implemented in MATLAB SIMULINK Plenty of examples case studies and simulation test benches covering the different stages of the design flow of sigma delta modulators A number of electronic resources including SIMSIDES the statistical data used in the state of the art survey as well as many design examples and test benches are hosted on a companion website Essential reading for Researchers and electronics engineering practitioners interested in the design of high performance data converters integrated in nanometer CMOS technologies mixed signal designers

CMOS Continuous-Time Adaptive Equalizers for High-Speed Serial Links Cecilia Gimeno Gasca, Santiago Celma Pueyo, Concepción Aldea Chagoyen, 2014-09-22 This book introduces readers to the design of adaptive equalization solutions integrated in standard CMOS technology for high speed serial links Since continuous time equalizers offer various advantages as an alternative to discrete time equalizers at multi gigabit rates this book provides a detailed description of continuous time adaptive equalizers design both at transistor and system levels their main characteristics and performances The authors begin with a complete review and analysis of the state of the art of equalizers for wireline applications describing why they are necessary their types and their main applications Next theoretical fundamentals of continuous time adaptive equalizers are explored Then new structures are proposed to implement the different building blocks of the adaptive equalizer line equalizer loop filters power comparator etc The authors demonstrate the design of a complete low power low voltage high speed continuous time adaptive equalizer Finally a cost effective CMOS receiver which includes the proposed continuous time adaptive equalizer is designed for 1.25 Gb/s optical communications through 50 m length 1 mm diameter plastic optical fiber POF

Integrated Wide-Bandwidth Current

Sensing Tobias Funk, Bernhard Wicht, 2020-09-04 This book provides readers with a single source reference to current sensing integrated circuit design. It is written in handbook style including systematic guidelines and implementation examples. The authors focus on the implementation of wide bandwidth current sensing on a single microchip toward usage in applications such as sensing control and optimization of the energy flow in growth areas like industrial electronics, renewable energies, smart grids, electromobility, and the Internet of Things. Provides readers with a comprehensive all in one source for current sensing integrated circuit design including implementation examples. Discusses modeling and optimization of on chip Rogowski coil and Hall sensor in both lateral and vertical orientation. Includes noise reduction techniques such as auto zeroing and chopping. Covers open loop and closed loop sensor front end design. Presents the first on chip current sensor with a planar coil placed besides a power line to measure internal signal currents and the first off chip current sensor with a helix shaped coil for external signal currents in the multi MHz region.

High-Speed Optical Receivers with Integrated Photodiode in Nanoscale CMOS Filip Tavernier, Michiel Steyaert, 2011-06-20 This book describes the design of optical receivers that use the most economical integration technology while enabling performance that is typically only found in very expensive devices. To achieve this, all necessary functionality from light detection to digital output is integrated on a single piece of silicon. All building blocks are thoroughly discussed, including photodiodes, transimpedance amplifiers, equalizers, and post amplifiers.

Precision Instrumentation Amplifiers and Read-Out Integrated Circuits Rong Wu, Johan H. Huijsing, Kofi A. Makinwa, 2012-07-25 This book presents innovative solutions in the design of precision instrumentation amplifier and read out ICs which can be used to boost millivolt level signals transmitted by modern sensors to levels compatible with the input ranges of typical Analog to Digital Converters (ADCs). The discussion includes the theory, design, and realization of interface electronics for bridge transducers and thermocouples. It describes the use of power efficient techniques to mitigate low frequency errors resulting in interface electronics with high accuracy, low noise, and low drift. Since this book is mainly about techniques for eliminating low frequency errors, it describes the nature of these errors and the associated dynamic offset cancellation techniques used to mitigate them.

Emerging Memory Technologies Yuan Xie, 2013-10-21 This book explores the design implications of emerging non-volatile memory (NVM) technologies on future computer memory hierarchy architecture designs. Since NVM technologies combine the speed of SRAM, the density of DRAM, and the non-volatility of Flash memory, they are very attractive as the basis for future universal memories. This book provides a holistic perspective on the topic, covering modeling, design, architecture, and applications. The practical information included in this book will enable designers to exploit emerging memory technologies to improve significantly the performance, power, reliability of future mainstream integrated circuits.

Monolithic Integration in E-Mode GaN Technology Maik Peter Kaufmann, Bernhard Wicht, 2022-10-26 This book is a comprehensive all in one source on design of monolithic GaN power ICs. It is written in handbook style with systematic guidelines and includes implementation examples. It covers the full range from

technology fundamentals to implementation details including design techniques specific for GaN technology It provides a detailed loss analysis based on comparative measurements between silicon and GaN based converters to provide an understanding of the relations between design choices and results which can be transferred to other power converter systems

Advances in Computational Intelligence Ignacio Rojas,Gonzalo Joya,Andreu Catala,2017-06-04 This two volume set LNCS 10305 and LNCS 10306 constitutes the refereed proceedings of the 14th International Work Conference on Artificial Neural Networks IWANN 2017 held in Cadiz Spain in June 2017 The 126 revised full papers presented in this double volume were carefully reviewed and selected from 199 submissions The papers are organized in topical sections on Bio inspired Computing E Health and Computational Biology Human Computer Interaction Image and Signal Processing Mathematics for Neural Networks Self organizing Networks Spiking Neurons Artificial Neural Networks in Industry ANNI 17 Computational Intelligence Tools and Techniques for Biomedical Applications Assistive Rehabilitation Technology Computational Intelligence Methods for Time Series Machine Learning Applied to Vision and Robotics Human Activity Recognition for Health and Well Being Applications Software Testing and Intelligent Systems Real World Applications of BCI Systems Machine Learning in Imbalanced Domains Surveillance and Rescue Systems and Algorithms for Unmanned Aerial Vehicles End User Development for Social Robotics Artificial Intelligence and Games and Supervised Non Supervised Reinforcement and Statistical Algorithms

Ultra Low Power Bioelectronics Rahul Sarpeshkar,2010-02-22 This book provides for the first time a broad and deep treatment of the fields of both ultra low power electronics and bioelectronics It discusses fundamental principles and circuits for ultra low power electronic design and their applications in biomedical systems It also discusses how ultra energy efficient cellular and neural systems in biology can inspire revolutionary low power architectures in mixed signal and RF electronics The book presents a unique unifying view of ultra low power analog and digital electronics and emphasizes the use of the ultra energy efficient subthreshold regime of transistor operation in both Chapters on batteries energy harvesting and the future of energy provide an understanding of fundamental relationships between energy use and energy generation at small scales and at large scales A wealth of insights and examples from brain implants cochlear implants bio molecular sensing cardiac devices and bio inspired systems make the book useful and engaging for students and practicing engineers

Artificial Intelligence and Hardware Accelerators Ashutosh Mishra,Jaekwang Cha,Hyunbin Park,Shiho Kim,2023-03-15 This book explores new methods architectures tools and algorithms for Artificial Intelligence Hardware Accelerators The authors have structured the material to simplify readers journey toward understanding the aspects of designing hardware accelerators complex AI algorithms and their computational requirements along with the multifaceted applications Coverage focuses broadly on the hardware aspects of training inference mobile devices and autonomous vehicles AVs based AI accelerators

Smart Card Research and Advanced Applications Josep Domingo-Ferrer,Joachim Posegga,Daniel Schreckling,2006-03-28 This volume constitutes the

refereed proceedings of the 7th International Conference on Smart Card Research and Advanced Applications CARDIS 2006 held in Tarragona Spain in April 2006 The 25 revised full papers presented were carefully reviewed and updated for inclusion in this book The papers are organized in topical sections on smart card applications side channel attacks smart card networking cryptographic protocols RFID security and formal methods **Temperature- and Supply**

Voltage-Independent Time References for Wireless Sensor Networks Valentijn De Smedt, Georges Gielen, Wim Dehaene, 2014-11-07 This book investigates the possible circuit solutions to overcome the temperature and supply voltage sensitivity of fully integrated time references for ultra low power communication in wireless sensor networks The authors provide an elaborate theoretical introduction and literature study to enable full understanding of the design challenges and shortcomings of current oscillator implementations Furthermore a closer look to the short term as well as the long term frequency stability of integrated oscillators is taken Next a design strategy is developed and applied to 5 different oscillator topologies and 1 sensor interface All 6 implementations are subject to an elaborate study of frequency stability phase noise and power consumption In the final chapter all blocks are compared to the state of the art **RF CMOS Oscillators for Modern Wireless Applications** Masoud Babaie, Mina Shahmohammadi, Robert Bogdan Staszewski, 2022-09-01 While mobile phones enjoy the largest production volume ever of any consumer electronics products the demands they place on radio frequency RF transceivers are particularly aggressive especially on integration with digital processors low area low power consumption while being robust against process voltage temperature variations Since mobile terminals inherently operate on batteries their power budget is severely constrained To keep up with the ever increasing data rate an ever decreasing power per bit is required to maintain the battery lifetime The RF oscillator is the second most power hungry block of a wireless radio after power amplifiers Consequently any power reduction in an RF oscillator will greatly benefit the overall power efficiency of the cellular transceiver Moreover the RF oscillators purity limits the transceiver performance The oscillator s phase noise results in power leakage into adjacent channels in a transmit mode and reciprocal mixing in a receive mode On the other hand the multi standard and multi band transceivers that are now trending demand wide tuning range oscillators However broadening the oscillator s tuning range is usually at the expense of die area cost or phase noise The main goal of this book is to bring forth the exciting and innovative RF oscillator structures that demonstrate better phase noise performance lower cost and higher power efficiency than currently achievable Technical topics discussed in RF CMOS Oscillators for Modern Wireless Applications include Design and analysis of low phase noise class F oscillators Analyze a technique to reduce 1 f noise up conversion in the oscillators Design and analysis of low power low voltage oscillators Wide tuning range oscillators Reliability study of RF oscillators in nanoscale CMOS **Automotive Radar Sensors in Silicon Technologies** Vipul Jain, Payam Heydari, 2012-09-27 One of the leading causes of automobile accidents is the slow reaction of the driver while responding to a hazardous situation State of the art wireless electronics can automate several driving

functions leading to significant reduction in human error and improvement in vehicle safety With continuous transistor scaling silicon fabrication technology now has the potential to substantially reduce the cost of automotive radar sensors This book bridges an existing gap between information available on dependable system architecture design and circuit design It provides the background of the field and detailed description of recent research and development of silicon based radar sensors System level requirements and circuit topologies for radar transceivers are described in detail Holistic approaches towards designing radar sensors are validated with several examples of highly integrated radar ICs in silicon technologies Circuit techniques to design millimeter wave circuits in silicon technologies are discussed in depth

Chip-Scale Power Supplies for DC-Link and Grid Applications Christoph Rindfleisch, Bernhard Wicht, 2024-06-25 This book is a comprehensive single source on the design of chip scale high voltage power supplies for low power DC link and grid applications It is written in handbook style with systematic guidelines and includes implementation examples The authors cover the full range from technology fundamentals to circuit implementation details The book includes guidelines for the application specific selection of the converter topology design guidelines for the inductive components and a detailed description of low power optimized control approaches and subcircuits The authors also include guidelines for the selection and design of high voltage on chip power switches and for the reduction of parasitic effects such as capacitive losses

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