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Basic Electricity Electronics Vol 1 Basic Principles Applications

Euzeli dos Santos, Edison R. da Silva



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Power Integrity for Electrical and Computer Engineers J. Ted Dibene, II,David Hockanson,2019-09-24 A professional guide to the fundamentals of power integrity analysis with an emphasis on silicon level power integrity Power Integrity for Electrical and Computer Engineers embraces the most recent changes in the field offers a comprehensive introduction to the discipline of power integrity and provides an overview of the fundamental principles Written by noted experts on the topic the book goes beyond most other resources to focus on the detailed aspects of silicon and optimization techniques in order to broaden the field of study This important book offers coverage of a wide range of topics including signal analysis EM concepts for PI frequency domain analysis for PI numerical methods overview for PI and silicon device PI modeling Power Integrity for Electrical and Computer Engineers examine platform technologies system considerations power conversion system level modeling and optimization methodologies To reinforce the material presented the authors include example problems This important book Includes coverage on convergence accuracy and error analysis and explains how these can be used to analyze power integrity problems Contains information for modeling the power converter from the PDN to the load in a full system level model Explores areas of device level modeling of silicon as related to power integrity Contains example word problems that are related to an individual chapter s subject Written for electrical and computer engineers and academics Power Integrity for Electrical and Computer Engineers is an authoritative guide to the fundamentals of power integrity and explores the topics of power integrity analysis power integrity analytics silicon level power integrity and optimization techniques **Paperbound Books in Print** ,1991 **Reliability of high-k / metal gate field-effect transistors considering circuit operational constraints** Steve Kupke,2016-06-06 After many decades the scaling of silicon dioxide based field effect transistors has reached insurmountable physical limits due unintentional high gate leakage currents for gate oxide thicknesses below 2 nm The introduction of high k metal gate stacks guaranteed the trend towards smaller transistor dimensions The implementation of HfO₂ as high k dielectric also lead to a substantial number of manufacturing and reliability challenges The deterioration of the gate oxide properties under thermal and electric stress jeopardizes the circuit operation and hence needs to be comprehensively understood As a starting point 6T static random access memory cells were used to identify the different single device operating conditions The strongest deterioration of the gate stack was found for nMOS devices under positive bias temperature instability PBTI stress resulting in a severe threshold

voltage shift and increased gate leakage current A detailed investigation of physical origin and temperature and voltage dependency was done The reliability issues were caused by the electron trapping into already existing HfO₂ oxygen vacancies The oxygen vacancies reside in different charge states depending on applied stress voltages This in return also resulted in a strong threshold voltage and gate current relaxation after stress was cut off The reliability assessment using constant voltage stress does not reflect realistic circuit operation which can result in a changed degradation behaviour Therefore the constant voltage stress measurement were extended by considering CMOS operational constraints where it was found that the supply voltage frequently switches between the gate and drain terminal The additional drain off state bias lead to an increased V_t relaxation in comparison to zero bias voltage The off state influence strongly depended on the gate length and became significant for short channel devices The influence of the off state bias on the dielectric breakdown was studied and compared to the standard assessment methods Different wear out mechanisms for drain only and alternating gate and drain stress were verified Under drain only stress the dielectric breakdown was caused by hot carrier degradation The lifetime was correlated with the device length and amount of subthreshold leakage The gate oxide breakdown under alternating gate and o state stress was caused by the continuous trapping and detrapping behaviour of high k metal gate devices

Scientific and Technical Aerospace Reports ,1992 **ELECTRICAL ENGINEERING - Volume III** Wong Kit Po ,2009-12-13 Electrical Engineering is the component of Encyclopedia of Physical Sciences Engineering and Technology Resources in the global Encyclopedia of Life Support Systems EOLSS which is an integrated compendium of twenty one Encyclopedias The Theme on Electrical Engineering with contributions from distinguished experts in the field provides the essential aspects and fundamentals of electrical engineering These three volumes are aimed at the following five major target audiences University and College Students Educators Professional Practitioners Research Personnel and Policy Analysts Managers and Decision Makers NGOs and GOs *Trade and Industrial Education; Instructional Materials* Ohio State University. Center for Vocational and Technical Education,1972 Understanding Power Electronics via Simulations S. Raghuram Naidu,2025-08-05 This book introduces Power Electronics by emphasizing computer simulation using the Python programming language It is primarily a senior undergraduate text but can also be used for a post graduate course on DC DC switch power mode converters It is also a reference for practicing engineers Theoretical analysis of the circuits and approximate calculations precede the simulations and the reader is taken by hand through the numerical analysis Finally when the programs are presented they are simple and easy to understand Python is a computational language that is easy to learn and has the libraries necessary for simulations The author has observed that students interest in Power Electronics is enhanced when they learn to simulate the circuits without depending on a general purpose program This book encourages the reader to run the programs and perform numerical experiments Quite a few simulations in the later chapters have been left as exercises for the reader Many feedback control issues have been extensively discussed in the chapter on non isolated

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